# Nacro TME 6502 תロURNA 



No 4

##  <br> MUCRロCMESS邑回いて㘣 INPORMATMON  MAALING 凸IST MACRODADR そE凸P RELAY

 POWER PL凹S
## MEMORY PLUS

## ENCLOSURP P凸யS

WHILE MOST OF THE ABOVE PRODUCTS ARE AIMED AT THE KIM－1，MANY OF THEM CAN BE EASILY ADAPTED TO WORK WITH ÓTHER 6502 BASED SYSTEMS．

IF YOUR LOCAL 6502 DEALER DOES NOT CARRY THESE PRODUCTS，TELL HIM TO CONTACT US FOR OUR DEALER INF ORMATION PACKAGE．

FOR A COPY OF OUR CURRENT CATALOG，WHICH COVERS ALL OF OUR PRODUCTS IN DETAIL，PLEASE SENO A LABEL WITH YOUR NAME AND ADDRESS（the label from the MICRO envelope will be fine）AND A 13 CENT STAMP （or 4 International Response Coupons）TO：

The COMPUTERIST
P．0．Box 3
S．Chelmsford，MA 01824
Apple II Variables Chart ..... 4by C. R. Carpenter
The PET Vet Examines Some BASIC Idiosyncrasies ..... 5by Charles Floto
A Complete Morse Code Send/Receive Program for the KIM-1 ..... 7by Marvin L. De Jong
Early PET-Compatible Products ..... 22
by Charles Floto
PET Software From Commodore ..... 21 by Roy $0^{\prime}$ Brien
The MICRO Software Catalog ..... 23by Mike Rowe
Apple II Printing Update ..... 27by C. R. Carpenter
MICRO STUFF and MICROBES ..... 30
Standard 6502 Assembly Syntax? ..... 31 by Hal Chamberlin
Worm in the Apple? ..... 32by Mike Rowe
Writing for MICRO and MICRO Manuscript Cover Sheet ..... 33
6502 Bibliography - Part III ..... 35
by William Dial
A KIM Beeper ..... 43
by Gerald C. Jenkins
An Apple II Programmer's Guide ..... 45by Rick Auricchio
Advertisers Index

| The COMPUTERIST | IFC | The COMPUTERIST | 26 |
| :--- | ---: | :--- | ---: |
| The Computer Store | 2 | Computer Components | 29 |
| Riverside Electronics | 12 | A B Computers | 30 |
| CGRS | 21 | K L POwer Supplies | 33 |
| Micro Technology Unlimited | 21 | the enclosures group | 44 |

MICRO is published bi-monthly by The COMPUTERIST, 8 Fourth Lane, So. Chelmsford, MA 01824. Robert M. Tripp, Editor/Publisher. Controlled circulation postage paid at Chelmsford, Massachusetts.

Single Copy: $\$ 1.50$ Annual Subscription: $\$ 6.00$ ( 6 issues) in USA.
Copyright 1978 by The COMPUTERIST. All Rights Reserved.

# the Computer Store 

63 SOUTH MAIN STREET, WINDSOR LOCKS, CONNECTICUT 06096


The Computer Store is pleased to announce off-the-shelf availability of Apple II, the personal computer.


The feature article in this issue is "A Complete Morse Code Send/Receive Program for the KIM-1" by Marvin L. De Jong [page 7]. Marvin has had two excellent articles in previous issues of MICRO [Digital-Analog and Analog-Digital Conversion Using the KIM-1, MICRO \#2, and, Employing the KIM-1 Microcomputer as a Timer and Data Logging Module, MICRO \#3]. His new article, which includes eight pages of source listings should be of interest to all 6502 programmers, even those with zero interest in ham radio. There are a number of useful techniques in the program:
a bit pattern conversion;
a table lookup;
some interrupt handling;
use of the KIM timer
just to mention a few. The ham radio enthusiast will, of course, find a lot of other good stuff, and will probably want to try it with their own equipment.
"The Apple II Chart" [page 4] was submitted by another MICRO regular, C. R. (Chuck) Carpenter. Chuck recommends that the chart be used to layout and keep track of strings for Applesoft. BASIC. He suggests making two copies of the page, one for alphabetic and one for numeric variables, placing them between two sheets of plastic, and writing on the plastic with a felt tip pen so that the setup can be erased and used over again.
cnuck has also written the "Apple II Printing Update" [page 27] as a follow on to nis article on "Printing with the Apple II", MICRO \#3. Here he presents solutions to a couple of problems he encountered, plus a short note on how to let BASIC do hex-to-decimal conversions for you.

Charles Floto, with a little help from his friends, continues to provide info about the PET. "The PET Vet Examines some BASIC Idiosyncrasies" [page 5] has a discussion of some of the features of a Mailing List Program which was written by Richard Rosner. Charles also discusses some "Early PET-compatible Products" [page 22]. Roy o'Brien assembled a short list of "PET Software from Commodore" [page 21] which covers
software and documentation which you may be able to get directly from Commodore if you ask for it nicely.

The extensive "6502 Bibliography" being compiled by William Dial, is continued. Part I [MICRO \#1] covered references 1 through 128; Part II [MICRO \#3] covered 129 through 179; and Part III continues through reference 300. Suddenly there seems to be a lot of material being written on the 6502. It looks like the secret of what a great little processor it is has gotten "out of the bag". If you know of any source of regular info on 6502 s that Bill is not covering, how about letting him know about it and perhaps he can get on the subscription or distribution list and include the material in future "6502 Bibliography" parts.
since a "beeper" for the PET is mentioned in one of this issues articles, and since the Apple II already has a built in beeper, it only seemed fair to give the KIM-1 a voice too. Gerald C. Jenkins presents "A Kim Beeper" [page 43] that is easy to build and provides the software to run it.
"The MICRO Software Catalog" [page 23], begins in this issue, and will probably become a regular department. A number of items were received too late for inclusion in this issue, and will be held over for the next issue. Certain items were considered to be too small or of limited interest to be included. We will return these to the senders so that they will know the status of their submission.

While MICRO likes to "accentuate the positive", we would be remiss if we would totally "eliminate the negative". A potentially serious problem with the Apple II has been raised, and a brief discussion is presented in "A Worm in the Apple" [page 32]. We will follow up on this item and present more info next issue.

Rick Auricchio presents "An Apple II Programmer's Guide" [page 45] which sontains a lot of information he has discovered which the manual did not cover. Included in the article are a pair of tables which Apple programmers will find useful.
AFPLE II VARIABLES FOR APPLE SOFT BASIC


 O OO O1 O2 03 O4 O5 O6 O7 OB O9 OA OE OC OU OE OF OG OH OI OJ OK OL OM ON OO OF OQ OR OS OT OU OU OW OX OY OZ


 S SO 51 S2 53 S4 55, S6 ST 58 S 5 SA SE SC SD SE SF SG SH SI SJ SK SL SM SN SO SF SQ SE SS ST SU SU SW SX SY SZ


 W WO WI W2 W3 W4 W5 WG W7 WE Wg WA WE WC WI WE WF WG WH WI WJ WK WL WM WN WO WP WO WR WS WT WU WU WW WX WY WZ




# THE PET VET EXAMIEES SOHE BASIC IDIOSYCRASIES 

## Charles Floto

325 Pennsylvania Ave., S.E. Washington, DC 20003

Richard Rosner has supplied a program listing produced using his HS-232 printer interface for the PET. As it's well commented I'll only point out examples of some of the unusual features of PET BASIC.

Line 1 is an example of the OPEN statement. The first number specifies that it applies to logical file number 5 . This is the name by means of which other statements in the program will use this data file. The second number specifies that physical device number 5 is being used. Which device is number 5 is determined by the wiring of the system.

The PET, as sold, is wired for device 0 the keyboard; 1, the built-in tape drive; 2, the auxiliary drive connector on the back; and 3, the screen. Referring to a physical device that hasn't been electrically connected will result in a DEVICE NOT PRESENT ERROR. Richard's system does contain a physical device 5: his RS-232 output port.

If the third number in the OPEN statement is 0 , reading the file is enabled. Writing is prepared for by 1 , while a 2 here enables file writing with an end-of-tape character to be added when the file is CLOSEd.

Line 2 illustrates the use of CMD. It allows program commands to be applied to a device specified by the logical file connected with it (not by the physical device number). Note that RUN will merely cause a listing to be produced. RUN 5 calls the rest of the program into action.

Line 2000 demonstrates use of the OPEN statement with a variable. Lines 20002300 print data either on the tape drive or on the screen depending on which device number is the current value of variable D. In each case logical file 8 is used.

Another idiosyncrasy comes up here: while PRINT may be entered as ?, PRINT\# cannot be entered as ?\# - it must be spelled out. Otherwise a SYNTAX ERROR will result when the program is run, even though the listing will look alright.

But you can still save a good deal of typing entering these lines. Once 2110 is in simply move the cursor up to change the line number to 2111 and NA to AD. Then hit RETURN and you'll have both 2110 and 2111 in memory.

I suggest you make a few changes in Richard's program. Add 105 DIM ST\$(CO) Consider storing the zip code as a string rather than as an integer. Re peat lines $2000-2300$ as $5000-5300$ (by changing the first digit in each line number) and change line 4500 accordingly. Then you can alter the display format without messing up the tape format. And remember that you can slow screen printing by holding the RVS key down.

A final note: I understand Commodore is now using a different tape drive and recording system. This may create compatibility problems in exchanging programs between the early PETs and the later ones.

1 OPEN 5.5.1, "Mailing List Program (Incomplete)"
2 CMD5:PRINT"":LIST:END
5 REM the above lines list the program on the hard copy unit
$1 \varnothing$ REM
11 REM WRITTEN BY RICHARD ROSNEK
12 REM BKOOKFIELD, CONN.
13 REM $F() R$ THE COMM(ODOKE PET.
14 REM PRINTED ()N A GE PRINTER
15 REM USING A PET ADA AVAILABLE FKOM THE AUTHOK.
49 REM $D=D E V I C E C O D E$
$50 \mathrm{D}=1$ ：REM TAPE DRIVE \＃ 1
）（ $) \mathrm{C}(0=5$（ $)$
11 REM C（O）＝MAX N（）．OF PECORES IN LIST
$10 \Leftrightarrow$ DIM NAS（CO）．ADS（CO），CI $\leqslant(C())$
1 ©1 REM $N A S=N A M E, A D S=A D D R E S S, C I S=C I T Y$
1 И2 REM STS＝STATE，Z＝ZIP CODE
In3 REM KC＝KEY CODE．UP TO I I FOR EACH ADDRESS
110 DIM Z（CO）．KC\％（1日．CO）
997 REM FNTER RECORDS FOR MAILING LIST
998 KEM EXIT ON＇！！FOR NAME
1and FOR N＝（A TO CO
1010 INPUT＂NAME＂：NAS（N）
1020 IF NAS（N）＝＂！＂GOTO 20日G
$1025 \mathrm{LN}=\mathrm{N}$
103n INPUT＂ADCKESS＂；AD）\＄（N）
164И INPUT＂CITY，STATE＂；CI \＄（N），ST\＄（N）
1050 INPUT＂ZIP CODE＂；Z（N）
1000 FOR N1＝0 TO 10
1070 PRINT＂KEY\＃＂；NI；：INPUT KC\％（NI，N）
1 （880 IF KC\％（NI，N）＝0 GOTO 118Ø
1100 NEXTNI
1180 NEXT N
1998 P！RINT ON TAPE DRIVE（D＝1）OR SCREEN（ $D=3$ ）
2ด0ด OPEN 8，D．1．＂ADDRESS FILE＂
2 （ดด9 KEM LN＝NUMBER OF RECORDS
201の PRINT\＃8，LN
210 FOR N＝$\because$ T $)$ LN
2110 PRINT\＃8，NAS（N）
2111 PKINT\＃8，ADs（N）
2112 PRINT\＃8，CIS（N）
2113 PRINT\＃8．sTs（N）
2115 PKINT\＃8，Z（N）
2126 FOR NI＝0 TO 10
2130 P！RINT\＃8．KC\％（NI，N）
2150 NEXT NI
2200 NEXT N
230n CLOSE 8
3Иロ日 END
3997 REM ENTEK AT 4 4 （甘め TO KEAD IN FP（OM TAPE
3998 KEM DKIVE NO．I AND THEN PRINT ON SCREEN
4ดดØ OPEN 8．1．И，＂ADDRESS FILE＂
4め1め INPUT\＃8．LN
4めI। PRINTLN：KEM．PRINT RECOKD COUNT
41 日U FOR N＝O TO LN
4116 INPUT\＃8．NAS（N）
4120 REM IF STI AND 64 GOTO 430N
4130 INPUT\＃8，ADS（N）
4131 INPUT\＃8．CIS（N）
4132 INPUT\＃8，ST\＄（N）
4135 INPUT\＃8，Z（N）
$4140 \mathrm{FO} \mathrm{O} \mathrm{NI}=\mathrm{G}$ TO 10
4150 INPUT\＃8，KC\％（N1，N）
4160 NEXTN
4190 PRINTN：KEM PRINT RECORD NO．AS KEAD
4200 NEXT N
4300 CLOSE 8
4500 $\mathrm{D}=3:$ GOTO 2 ann
READY．

## A COMPLETE MORSE CODE SEMD/RECEIVE PROGRAM FOR THE EIM-1

Marvin L. De Jong, KOEI<br>Dept. of Math-Physics<br>The School of the Ozarks<br>Point Lookout, MO 65726

## I. INTRODUCTION

The program described below will convert ASCII from a keyboard to a Morse code digital signal which can be used to key a transmitter. It will also convert a Morse code digital signal to ASCII for display on the user's video system. Suitable references for circuits to convert the audio signal from a communications receiver to a digital Morse signal are also given. [1,2]

The entire program resides in the memory on the KIM-1, and has the following features:

1. The precise code speed in words per minute can be entered at any time from the keyboard. Key in CONTROL $S$ followed by any two-digit decimal number from 05 to 99 words per minute.
2. The operator can type as many as 256 characters ahead of the character currently being sent. One page of memory is devoted to a FIFO buffer.
3. When there are less than 16 characters left in the buffer, the KIM-1 display indicates how many characters are left ( $F$ to 0 hex).
4. Backspace capability is provided. CONTROL B erases the last character entered into the buffer, and the operator then enters the correct character.
5. The buffer can be pre-loaded with as many characters (up to 256) as desired while the program is in the receive mode. Pressing CONTROL G starts the program sending code as soon as the operator is ready.
6. CONTROL $R$ sends the program from the send mode to the receive mode.
7. While in the receive mode the display on the KIM-1 informs the operator to either increase the code speed ( F , for faster, on the display) or decrease ( S , for slower) the speed for proper reception. The receive program actually tolerates a large range in code speeds with no adjustment.
8. The feature just mentioned can be used to measure the "other guy's" code speed.
9. If the receive mode is not used, any CONTROL key not mentioned above will put the program in an idle loop so the buffer can be loaded. CONTROL G starts the message.
10. The carriage return key restarts the send program, or it can be returned from the receive mode to the send mode with CONTROL G.

The KIM-1 was first programmed to send code by Pollock [3], and some of the features of his program are found here. Pollock [4] has also described a microprocessor controlled keyboard using the 6504. It has more features than his original program written for the KIM-1, but the program described here has some additional features which are very attractive, especially the receive program.

## II. BACKGROUND

## A. Sending Morse Code (ASCII to Morse)

A negative going 10 microsecond strobe pulse from the keyboard is connected to the NMI pin on the KIM-1. Whenever a key is pressed an NMI interrupt occurs and the ASCII code from the keyboard is read at the lowest 7 pins of port $A$ (PAD). The eighth bit is held high, so the number read is actually the ASCII code plus 80 hex. This number is stored in the FIFO buffer which is page 2 of memory on the KIM-1. The send routine uses the numbers in the FIFO memory to index a location in page zero which contains the information to construct the Morse character.

An illustration will make this clear. The ASCII hex representation of the letter $C$ is 43 . The strobe pulse causes port A to be read, which results in the number C 3 ( $\mathrm{C} 3=43+80$ ) being stored in the FIFO. When the send routine gets to the location in the FIFO where C3 is stored, it uses it to
locate the contents of address 00C3. In location C3 in zero page is found 1A which is 00011010 in binary. The most significant 1 is simply a bit which indicates that all lesser significant bits contain the code information, namely $1=$ dash and $0=$ dot. Thus, $C$ is dash-dot-dash-dot (1010).

The program causes the 00011010 to be rotated left (ROL) until a 1 appears in the carry position. The carry flag set causes the program to analyze the remaining bits for their code content. It does this by successively rotating them (ROL) into the carry position. If a 1 appears in the carry position, PBO is held at logical 1 for the appropriate time followed by a space while PBO is at logical 0 . If a 0 appears in the carry position a dot is sent, followed by a space. When a total of 8 ROL commands have been completed, counting those needed to find the leading 1 , then PBO is held at logical 0 for an additional time to give a character space. The space bar produces still more time at logical 0 to produce a word space.

CONTROL $S$ changes the NMI interrupt vectors so that the next two characters (hopefully decimal digits) from the keyboard are read, converted from base ten to hex [5], and converted to the basic time unit (see below). The interrupt vectors are then restored so that further characters from the keyboard are read as usual. Control characters are obtained by pressing the control key followed by the appropriate control character.

## B. Timing Considerations.

Before going much further, the timing calculations will be described. Morse code is a variable length code. That is, the number of bits is variable as contrasted to a fixed bit-length code such as ASCII. Its structure is based on the time duration of the various components as follows:

## Mark Elements:

Dot $=1 \mathrm{t}$
Dash $=3 t$

## Space Elements

Element space $=1$ t
(time between dots and dashes)
Character space $=3 \mathrm{t}$
(time between letters)
Word space $=7 \mathrm{t}$
(time between words)
The time $t$ depends on the code speed. According to The Radio Amateur's Handbook a code speed of 2.4 words per minute (wpm) corresponds to 10 dots per second. Since there are 10 element spaces included in the 10 dots per second, there are a total of 20 t in one second: that is, $t=1 / 20$ second at 24 wpm. At any other speed then

```
t = (1/20)(24/S)
    =(50 ms)(24;S)
    = (1200/S) in milliseconds (ms)
```

where $S$ is the code speed in wpm. If the divide-by-1024 timer on the KIM is used, 1 count corresponds to 1.024 ms . The number $T$ (called TIME in the program) to be loaded into the timer is then

$$
\begin{aligned}
T & =(1172 / \mathrm{S}) \text { base ten or } \\
& =(494 / \mathrm{S}) \text { hex. }
\end{aligned}
$$

The speed $S$ in wpm is entered in decimal from the keyboard, converted to base 16 (hex), sent to a divide routine to find T , and T is stored at 0000 in memory. $\quad 99$ wpm gives $O C$ hex in TIME while 05 wpm gives EB hex. Care was taken in developing the above calculations because of a discrepancy between it and the results given by Pollock[4].

The system timing was tested by comparing it with code sent by W1AW. The speeds are the same to better than one word per minute from 5 wpm to 35 wpm.

In the receiving program a word space is detected when a space counter exceeds 5T. At moderate code speeds 5 T is greater than 255 resulting in an overflow. Consequently, in the receive program $1 / 2 \mathrm{~T}$ is used as the basic time unit. In this case, speeds as low as 12 wpm can be received. At slower speeds the system still works, but word spaces occur between each letter.
C. Receiving Morse Code (Norse to ASCII)

To receive Morse code and convert it to ASCII, the inverse of the above process is carried out. It is assumed that a suitable audio detection circuit [1,2[ produces a logical 1 for a space element and a logical 0 for a mark element. This digital Morse signal is applied to PB7 and the IRQ pin on the KIM-1. A character register begins with a 1 in the zero bit position. Each time a dot is received the character register is shifted left and a zero is loaded into the character register. Each time a dash is received the character register is shifted left and a one is loaded into the zero bit position. Thus, when a character space is detected, and a (for example) has been received, the character register will contain 1 A , just as in sending a C. However, the 1 A is used to index a zero page location which contains the ASCII code for $C$, namely 43. The various components are identified by timing their duration.

## III. THE PROGRAMS

A detailed listing of the programs is given below. The detailed comments should allow the reader to understand, modify, and trouble-shoot the program.

## A. The Send Program

Some important variables, their meanings, and their locations in zero page are given:

Name Location Use
TIME 0000 TIME is the quantity $T$ mentioned in the section on timing considerations. It is the time, in units of 1.024 ms , of the dot or element space components.

SPEED 0013 SPEED is the hex equivalent of the number entered for the speed by the operator.

PNTR 0015
PNTR is a number which points to the location in the FIFO memory which contains the character currently being sent. The program idles as long as $Y=P N T R$, but begins to send when $Y$ exceeds PNTR.

Name Location Use
LO 001E Scratchpad location for division of 494 by SPEED to give TIME.

$$
\text { HI } \quad 001 \mathrm{~F} \quad \text { Same use as Lo. }
$$

CNTR 0022 CNTR keeps track of how many characters are left in the FIFO memory. A character entered decrements CNTR; a character sent increments CNTR.

CHEK 0024 Scratchpad location to count the number of numbers which have been entered after the control $S$ has been entered.

YREG 00F4 The Y register is used to point to the location in the FIFO memory where the last character entered from the keyboard is, namely 0200,Y.

## B. The Receive Program

Some important variables, their meanings, and their locations are given:

Name Location Use
XREG 00F5 The X register is the character register. It begins with a 1 in the 0-bit. It is shifted left for each mark element received and loaded with a 1 for a dash and a zero for a dot. Later it is used to index a table in zero page which has the ASCII code for the character.

MCNTZ 0054
If a mark element (dot or dash) is being received (PB7 and IRQ at logical 0) the mark counter is incremented at a rate of 1 count every 2.048 ms .

SCNTZ OOEE Same as mark counter except the incrementing occurs when a space is being detected (PB7 high and IRQ high). Rate is also 1 count every 2.048 ms .

HALFT 0051 If the SPEED is set correctly, the number of counts during a dot should be exactly $1 / 2$ TIME. This is the "dot length". If MCNTZ exceeds $1 / 2$ the dot length the program decides that a valid mark character has been received. HALFT is $1 / 2$ the dot length. A valid space element occurs when SCNTZ exceeds HALFT.

Name Location Use
TWOT TWOT is twice the dot length and is used to decide if a dot or a dash has been received. If MCNTZ exceeds TWOT the element is a dash, otherwise it is a dot.

FIVET 0053 FIVET is five times the dot length and is used to decide when a word space has been received.

## IV. INTERFACE

The keyboard strobe is connected to the NMI pin on the expansion connector on the KIM-1, and the 7 bit ASCII code from the keyboard goes to pins PAO-PA6, the low order bit to PAO and the high order bit to PA6. PA7 should be pulled up with a 10 K resistor.

The author's transmitter is a solidstate Triton IV and can be keyed with TTL IC's. The circuit diagram below indicates how it was connected to the KIM-1. Transmitters using grid-block keying or cathode keying cannot use this circuit. A relay driven by a Darlington pair connected to pin PBO should work. The KIM-1 manuals give the appropriate details.


GND
The audio from the receiver must produce a logical 0 at pin PB7 and the IRQ pin when a tone is detected, and a logical 1 at the same pins when a space is detected. The reader is urged to try either of the circuits found in references 1 and 2. I used a half-baked scheme in which the audio from the receiver was fed to a half-wave rectifier (diode), filtered slightly, and connected to the inverting input of a CA3140 op amp. The voltage at the noninverting input was adjustable. The op
amp was operated as an open-loop comparator with the output connected to pin PB7 and IRQ. An oscilloscope was necessary to monitor the output and make the necessary adjustments for various signal levels. I am not recommending this circuit for general use.

I have also tried using the tape-input PLL system on the KIM-1 to convert the receiver audio to a digital signal. To lower the free-running frequency of the VCO a shunt capacitor must be added. The digital signal appears at address 1742, bit 7. I had only marginal success, the problem being that the digital signal tends to drop out for very short periods of time, which clears the mark counter (instructions 039F-03A2). Substituting NOP's for these instructions seems to improve the performance, but receiver tuning and volume control adjustments are sensitive. Some users may wish to experiment with deleting the aforementioned instructions in whatever interface circuit they may use.

## V. MISCELLANEOUS REMARKS

To get the entire Send/Receive program in the KIM-1 memory extensive use was made of page 1. This is also used as the stack. Care was taken to leave enough room for the stack operations, and for insurance, there are several points in the program where the stack pointer is initialized to FF. No problems should be encountered once the program is up and running. If you have any debugging to do I suggest using the single-step mode (be sure to set the NMI vectors) to check the jumps and branches. My experience has been that errors in branches generally result in about half the program being wiped out, especially if it is in page 1 of memory.

Wouldn't it be nice if some outfit like The COMPUTERIST would offer an interface board which would provide an audio to digital Morse circuit, a relay driver and relay (reed type) for transmit, a DIP socket for a ribbon cable from the keyboard, and a DIP socket for the ASCII out (see appendix), all on a single board which would mate with the KIM-1 application socket.

The first time I operated the system, I answered a CQ on 40 meters from WB2GMN,

Hank, who has Army Signal Corps experience. Even though he rated his speed at 55 wpm he copied me at 60 wpm . Hank reported that the code sounded like perfect code (which it should be) and that it was very crisp at 60 wpm . 1t was a real coincidence to contact someone who had the capability to appreciate the keyboard system and to give an evaluation of its performance.

I hope that you enjoy working these programs. If you do not want the receive program, simply put in a JMP 0300 instruction (4C 00 03) starting at 0300. If you have any questions, feel free to write, enclosing a SASE for a response. I will try to answer any questions about interfacing the system to your station.

## References:

[1] Steber, G. R., and Reyer, S. E., "The Morse-A-Letter", Popular Electronics, January, 1977.
[2] Riley, T. P., "A Morse Code to Alphanumeric Converter and Display", in three parts, QST for October, November and December, 1975.
[3] Pollock, James W., " 1000 WPM Morse Code Typer", 73 Magazine, January, 1977.
[4] Pollock, James, W., "A Microprocessor Controlled CW Keyboard", Ham Radio, January, 1978.
[5] Ward, Jack, "Manipulating ASCII Data", Kilobaud, February, 1978.

ACSII to MORSE and MORSE to ASCII
Lookup Tables in Page Zero

| 00 | XX | 20 | 45 | 54 | 49 | 41 | 4 E | 4 D | 5 | 5 | 55 | 52 | 57 | 44 | 4 B | 47 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

Special Morse Characters
Keyboard Character

| $\overline{\mathrm{BT}}$ | $=$ |
| :---: | :---: |
| $\overline{\mathrm{SK}}$ | $\$$ |
| $\overline{\mathrm{AR}}$ | $\#$ |
| Space (Word) | Space Bar |



| 0082 | $8 D$ | 42 | 17 |
| :--- | :--- | :--- | :--- |
| 0085 | A9 | 80 |  |
| 0087 | $8 D$ | 40 | 17 |
| 008A | A0 | FF |  |
| 008C | 84 | 15 |  |
| $008 E$ | 84 | 22 |  |
| 0090 | C4 | 15 |  |
| 0092 | F0 | FC |  |
| 0094 | E6 | 15 |  |
| 0096 | A6 | 15 |  |
| 0098 | BD | 00 | 02 |
| $009 B$ | $4 C$ | 15 | 01 |


|  | STA | SBD | ON KIM-1 DISPLAY |
| :--- | :--- | :--- | :--- |
| LDAIM | $\$ 80$ | BLANK DISPLAY BY PUTTING 80 |  |
|  | STA | SAD | IN PORT SAD |
|  | LDYIM | \$FF | INIT Y POINTER |
|  | STYZ | PNTR | INIT SEND POINTER |
| LOOP | STYZ | CNTR | INIT BUFFER COUNTER |
|  | CPYZ | PNTR | IS Y = PNTR? |
|  | BEQ | LOOP | YES, IDLE UNTIL DIFFERENT |
|  | INCZ | PNTR | NO, INCR PNTR TO LOOKUP |
|  | LDXZ | PNTR | CHARACTER. PNTR = X INDEX |
|  | LDAX | FIFO | GET CHARACTER FROM FIFO |
|  | JMP | LOOPX | CONTINUE AT LOOPX |

DISPLAY SUBROUTINE

```
0 1 0 0
```

| 0100 | A6 | 22 |  |
| :--- | :--- | :--- | :--- |
| 0102 | E0 | 10 |  |
| 0104 | 90 | 08 |  |
| 0106 | A9 | 80 |  |
| 0108 | $8 D$ | 40 | 17 |
| $010 B$ | $4 C$ | 14 | 01 |
| $010 E$ | BD | E7 | $1 F$ |
| 0111 | $8 D$ | 40 | 17 |
| 0114 | 60 |  |  |

0115208017

## ORG $\$ 0100$

$$
0118 \text { E6 } 22
$$

$$
011 \mathrm{~A} 200001
$$

$$
011 \mathrm{D} 4 \mathrm{C} 9000
$$

| DISP | LDXZ | CNTR | TRANSFER CNTR TO X |  |
| :--- | :--- | :--- | :--- | :--- |
|  | CPXIM | \$10 | IS CNTR LESS THAN 10 HEX |  |
|  | BCC | OVER | YES, DISPLAY CNTR |  |
|  | LDAIM | \$80 | NO, BLANK DISPLAY |  |
|  | STA | SAD |  |  |
|  | JMP | THER |  |  |
| OVER | LDAX | TAB | FIND VALUE FROM KIM ROM |  |
|  | STA | SAD | TO DISPLAY CNTR |  |
| THER | RTS |  | RETURN |  |
|  |  |  |  |  |
| LOOPX | JSR | SEND | GO TO SEND TO OUTPUT CODE |  |
|  | INCZ | CNTR | INCR CNTR |  |
|  | JSR | DISP | DISPLAY IF LESS THAN 10 |  |
|  | JMP | LOOP | CONTINUE LOOP |  |

## INTERRUPT ROUTINES




| 01A 1 | 4C 3601 |  | JMP | BACK | RETURN TO MAIN PROGRAM |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 01A4 | C9 12 | ARND | CMPIM | \$12 | REMAINDER OF VCTL |
| 01A6 | D0 03 |  | BNE | TREE | CONTROL R? |
| 01A8 | 4 C 0003 |  | JMP | RCV | YES. GO TO RECEIVE PROGRAM |
| 01 AB | C9 0D | TREE | CMPIM | \$0D | CARRAIGE RETURN? |
| 01AD | D0 03 |  | BNE | BUF | BRANCH IF NOT |
| 01AF | 4C 5B 00 |  | JMP | RTN | YES. START MAIN PROGRAM |
| 01B2 | C9 07 | BUF | CMPIM | \$07 | CONTROL G? |
| 01B4 | F0 03 |  | BEQ | BRR | YES. RESET STACK POINTER AND G |
| 01B6 | 4 C B6 01 | IDLE | JMP | IDLE | TO LOOP. OR, IDLE HERE |
| 01B9 | A2 FF | BRR | LDXIM | \$FF | WHILE BUFFER IS LOADED |
| 01 BB | 9A |  | TXS |  | RESET STACK TOP |
| 01BC | $4 C 9000$ |  | JMP | LOOP | AND CONTINUE |

MORSE CODE RECEIVE PROGRAM
ORG \$0300

| 0300 | A9 90 |  | RCV | LDAIM | IRQ | SET IRQ VECTORS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 0302 | 8D FE | 17 |  | STA | IRLO |  |
| 0305 | A9 03 |  |  | LDAIM | IRQ | / PAGE ADDRESS |
| 0307 | 8D FF | 17 |  | STA | IRHI |  |
| 030A | A5 00 |  | CRK | LDAZ | TIME | SET DOT LENGTH BY GETTING |
| 030C | 4A |  |  | LSRA |  | TIME AND DIVIDING BY 2 |
| 030D | 8551 |  |  | STAZ | HALFT |  |
| 030F | 4651 |  |  | LSRZ | HALFT | HALFT HALFT IS $1 / 2$ DOT LENGTH |
| 0311 | 8552 |  |  | STAZ | TWOT |  |
| 0313 | 0652 |  |  | ASLZ | TWOT | TWOT IS TWICE DOT LENGTH |
| 0315 | 8553 |  |  | STAZ | FIVET |  |
| 0317 | OA |  |  | ASLA |  | MULTIPLY BY 4 |
| 0318 | OA |  |  | ASLA |  |  |
| 0319 | 18 |  |  | CLC |  |  |
| 031A | 6553 |  |  | ADCZ | FIVET | AND ADD 1 TIMES TO GET |
| 031C | 8553 |  |  | STAZ | FIVET | 5 TIMES DOT LENGTH |
| 031E | A9 00 |  |  | LDAIM | \$00 | CLEAR MARK AND SPACE |
| 0320 | 8554 |  |  | STAZ | MCNTZ | COUNTERS |
| 0322 | 85 EE |  |  | STAZ | SCNTZ |  |
| 0324 | 58 |  |  | CLI |  | ALLOW INTERRUPTS TO START |
| 0325 | A2 01 |  |  | LDXIM | \$01 | INIT CHARACTER REGISTER |
| 0327 | 4 C 27 | 03 | IDL | JMP | IDL | IDLE HER UNTIL MARK OCCURS |
| 032A | 2088 | 03 | AGN | JSR | TIMSET | START TIMER FOR SPACE COUNT |
| 032D | E6 EE |  |  | INCZ | SCNTZ | INCR SPACE COUNTER |
| 032F | A5 EE |  |  | LDAZ | SCNTZ | DOES IT EXCEED 1/2 DOT LENGTH? |
| 0331 | C5 51 |  |  | CMPZ | HALFT |  |
| 0333 | B0 08 |  |  | BCS | CHECK | YES, JUMP TO SET CHAR REGS |
| 0335 | AD 07 | 17 | WAIT | LDA | TMER | OTHERWISE WAIT FOR TIMER |
| 0338 | 10 FB |  |  | BPL | WAIT |  |
| 033A | 4 C 2 A | 03 |  | JMP | AGN | AND COUNT SPACES |
| 033D | 8A |  | CHECK | TXA |  | SHIFT CHAR REGISTER LEFT |
| 033E | OA |  |  | ASLA |  |  |
| 033F | AA |  |  | TAX |  |  |


| 0340 |  | 54 |  | LDAZ | MCNTZ | IF MARK COUNTER EXCEEDS TWICE |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 0342 |  | 52 |  | CMPZ | TWOT | THE DOT LENGTH, PUT ONE IN |
| 0344 | 90 | 03 |  | BCC | SKIP | CHAR REGISTER, OTHERWISE A ZERO |
| 0346 | E8 |  |  | INX |  |  |
| 0347 |  | 11 |  | BCS | FAT | IF A DASH, SKIP DISPLAY |
| 0349 | OA |  | SKIP | ASLA |  | IF A DOT, COMPARE WITH TIME |
| 034A |  | 00 |  | CMPZ | TIME | FOR SPEED INDICATOR |
| 034C | B0 0 | 07 |  | BCS | CAT |  |
| 034E |  | F1 |  | LDAIM | \$F1 | SHOW "F" IS DISPLAY |
| 0350 | 8D 40 | 4017 |  | STA | SAD |  |
| 0353 | 90 | 05 |  | BCC | FAT |  |
| 0355 | A9 E | ED | CAT | LDAIM | \$ED | SHOW "S" IN DISPLAY |
| 0357 |  | 4017 |  | STA | SAD |  |
| 035A | A9 0 | 00 | FAT | LDAIM | \$00 | CLEAR MARK COUNTER |
| 035C |  | 54 |  | STAZ | MCNTZ |  |
| 035E | AD 0 | 0717 | HOLD | LDA | 'TMER | WAIT FOR TIMER |
| 0361 | 10 F | FB |  | BPL | HOLD |  |
| 0363 | 208 | 8A 03 |  | JSR | TIMSET | START TIMER AGAIN |
| 0366 |  | EE |  | INCZ | SCNTZ | INCR SPACE COUNTER AGAIN |
| 0368 |  | EE |  | LDAZ | SCNTZ |  |
| 036A |  | 52 |  | CMPZ | TWOT | DOES SPACE COUNTER EXCEED TWICE |
| 036C |  | F0 |  | BCC | HOLD | THE DOT LENGTH. IF NOT, HOLD |
| 036E | 20 | CA 03 |  | JSR | CHAR | IF YES, PRINT CHARACTER |
| 0371 | A2 0 | 01 |  | LDXIM | \$01 | RESET CHAR REGISTER |
| 0373 | AD | 0717 | DOZE | LDA | TMER | WAIT FOR TIMER |
| 0376 |  | FB |  | BPL | DOZE |  |
| 0378 | 20 8 | 8A 03 |  | JSR | TIMSET | START TIMER AGAIN |
| 037B |  | EE |  | INCZ | SCNTZ | INCR SPACE COUNTER |
| 037D |  | EE |  | LDAZ | SCNTZ |  |
| 037F |  | 53 |  | CMPZ | FIVET | DOES SPACE COUNTER EXCEED FIVE TIMES |
| 0381 | 90 F | F0 |  | BCC | DOZE | DOT LENGTH. IF LESS, DOZE AGAIN |
| 0383 | 20 | CA 03 |  | JSR | CHAR | OTHERWISE PRINT SPACE |
| 0386 | 78 |  |  | SEI |  | PREVENT INTERRUPTS WHILE |
| 0387 | 4C | OA 03 |  | JMP | CRK | CHECKING SPEED SETTING |
| 038A | A9 | 20 | TIMSET | LDAIM | \$20 | LOAD TIMER FOR 2.048 MS |
| 038C | 8D | 0617 |  | STA | TIM |  |
| 038F | 60 |  |  | RTS |  | RETURN TO RCV PROGRAM |
| 0390 | 08 |  | IRQ | PHP |  | SAVE REGISTERS |
| 0391 | 48 |  |  | PHA |  |  |
| 0392 | 208 | 8A 03 |  | JSR | TIMSET | START TIMER |
| 0395 | AD | 0717 | LOAF | LDA | TMER | WAIT FOR TIMER |
| 0398 | 10 F | FB |  | BPL | LOAF |  |
| 039A | AD | 0217 |  | LDA | PBD | IS MARK SIGNAL PRESENT |
| 039D | 10 | 09 |  | BPL | OVER | YES, GO TO OVER |
| 039F | A9 0 | 00 |  | LDAIM | \$00 | NO, MUST HAVE BEEN NOISE |
| 03A1 | 855 | 54 |  | STAZ | MCNTZ | WHICH CAUSED INTERRUPT. RETURN |
| 03A3 | E6 | EE |  | INCZ | SCNTZ | TO COUNT SPACE AFTER RESETTING |
| 03A5 | 68 |  |  | PLA |  | MARK COUNTER TO ZERO |
| 03A6 | 28 |  |  | PLP |  |  |
| 03A7 | 40 |  |  | RTI |  | RETURN FROM INTERRUPT |


| 03A8 | 208 A 03 | OVER | JSR | TIMSET | Start timer again |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 03 AB | E6 54 |  | INCZ | MCNTZ | INCR MARK COUNTER |
| 03AD | A5 54 |  | LDAZ | MCNTZ | DOES MARK COUNTER EXCEED |
| 03AF | C5 51 |  | CMPZ | HALFT | 1/2 THE DOT LENGTH? |
| 03B1 | 90 E2 |  | BCC | LOAF | NO, GO LOAF AND CHECK MARK |
| 03B3 | A9 00 |  | LDAIM | \$00 | YES. CLEAR SPACE COUNTER |
| $03 \mathrm{B5}$ | 85 EE |  | STAZ | SCNTZ |  |
| $03 \mathrm{B7}$ | AD 0717 | KILTIM | LDA | TMER | CHECK TIMER |
| 03BA | 10 FB |  | BPL | KILTIM | KILL TIME |
| 03BC | AD 0217 |  | LDA | PBD | CHECK MARK SIGNaL ON PB7 |
| 03BF | 10 E7 |  | BPL | OVER | LOOP AGAIN IF STILL ON |
| 03 C 1 | 8A |  | TXA |  | SAVE S While Stack Pointer is set |
| 03 C 2 | A2 FF |  | LDXIM | \$FF | RESET TO TOP OF STACK |
| 03 C 4 | 9A |  | TXS |  |  |
| 03 C 5 | AA |  | TAX |  | RESTORE X |
| $03 \mathrm{C6}$ | 58 |  | CLI |  | CLEAR INTERRUPT FLAG SET EARLIER |
| $03 C 7$ | 4 C 2 A 03 |  | JMP | AGN | RETURN TO COUNT SPACE |
| 03CA | B5 00 | CHAR | LDAZX | ZTB | LOOKUP ASCII SYMBOL |
| 03CC | 8D FB 13 |  | STA | data | DATA IS VIDEO PORT IN AUTHORS |
| 03CF | A9 3F |  | LDAIM | \$3F | SYSTEM. THE REMAINDER OF THIS |
| 03D1 | 2D F9 13 |  | AND | CULO | SUBROUTINE INCREMENTS THE |
| 03D4 | C9 3F |  | CMPIM | \$3F | POSITION OF THE CURSOR TO PREPARE |
| 03D6 | 9011 |  | BCC | AHD | FOR THE NEXT CHARACTER |
| 03D8 | A9 1F |  | LDAIM | \$1F |  |
| 03DA | 2D FA 13 |  | AND | CUHI |  |
| 03DD | 18 |  | CLC |  |  |
| 03DE | 6901 |  | ADCIM | \$01 |  |
| 03 E | C9 20 |  | CMPIM | \$20 |  |
| 03E2 | 9002 |  | BCC | UP |  |
| 03 E 4 | A9 10 |  | LDAIM | \$10 |  |
| 03E6 | 8D FA 13 | UP | STA | CUHI |  |
| 03E9 | EE F9 13 | AHD | INC | CULO |  |
| 03EC | 60 |  | RTS |  |  |
|  |  | SEND S | UBROUTI |  |  |
| 1780 |  |  | ORG | \$1780 |  |
| 1780 | AA | SEND | TAX |  | A CONTAINS CHAR FROM FIFO |
| 1781 | B5 00 |  | LDAZX | ZTB | USE THIS TO LOOKUP MORSE |
| 1783 | 303 F |  | BMI | WDSP | SPACE BAR CHAR HAS 1 IN BIT 7 |
| 1785 | 18 |  | CLC |  | IF NOT MINUS, CLEAR CARRY FLAG and |
| 1786 | A2 08 |  | LDXIM | \$08 | SET UP X FOR 8 ROL INSTRUCTIONS |
| 1788 | 2A | RPT | ROLA |  | ROTATE LEFT UNTIL 1 APPEARS IN CARRY |
| 1789 | B0 06 |  | BCS | DWN | BRANCH IF 1 IN CARRY |
| 178B | CA |  | DEX |  | ELSE, DECREMENT X |
| 178 C | F0 35 |  | BEQ | OUT | IF $\mathrm{X}=0$, THEN DONE |
| 178E | 4C 8817 |  | JMP | RPT | ELSE CONTINUE |
| 1791 | CA | DWN | DEX |  | KEEP TRACK OF BITS TESTED |
| 1792 | 2A | BACK | ROLA |  | ROTATE A LEFT AND SAVE ON STACK |
| 1793 | 48 |  | PHA |  |  |
| 1794 | 8A |  | TXA |  | SAVE X ON STACK ALSO |
| 1795 | 48 |  | PHA |  |  |


| 1796 |  | 18 |  |  | BCS | DASH | DID ROTATE SET CARRY? IF YES, |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1798 |  | 01 |  |  | LDXIM | \$01 | SEND DASH, ELSE SEND DOT |
| 179A | EE | 02 | 17 | DAH | INC | PBD | PBO WILL BE LOGICAL 1 FO 1 T |
| 179D | 20 | C9 | 17 | SPA | JSR | TIMER | TIME GIVES DELAY OF TIME (1.024MS) |
| 17A0 | CA |  |  |  | DEX |  | ONE TIME UNIT IS UP |
| 17A1 | D0 | FA |  |  | BNE | SPA | IS $\mathrm{X}=0$ ? DELAY ANOTHER UNIT |
| 17A3 | AD | 02 | 17 |  | LDA | PBD | YES. NOW CHECK PBO. IF A 1 |
| 17A6 | 4A |  |  |  | LSRA |  | A SHIFT WILL SET CARRY FLAG |
| 17A7 | 90 | OC |  |  | BCC | DONE | IF CARRY CLEAR, THEN DONE |
| 17A9 | CE | 02 | 17 |  | DEC | PBD | OTHERWISE, SET PBO $=0$ FOR ELEMENT |
| 17AC | E8 |  |  |  | INX |  | SPACE FOR A DELAY OF 1 UNIT BY |
| 17 AD | 4C | 9D | 17 |  | JMP | SPA | RESETTING X AND LOADING TIMER |
| 17B0 | A2 | 03 |  | DASH | LDXIM | \$03 | DASH TAKES 3 TIME UNITS |
| 17B2 | 4 C | 9A | 17 |  | JMP | DAH | SEND 3 UNITS FOLLOWED BY SPACE |
| 17B5 | 68 |  |  | DONE | PLA |  | THEN ELEMENT IS DONE SO |
| 17B6 | AA |  |  |  | TAX |  | RESTORE A AND X AND GO BACK |
| 17B7 | 68 |  |  |  | PLA |  | IF X IS NOT ZERO |
| $17 \mathrm{B8}$ | CA |  |  |  | DEX |  | OTHERWISE ADD CHARACTER SPACE |
| 1789 | D0 | D7 |  |  | BNE | BACK | BY RUNNING TIMER FOR |
| 17BB | A2 | 02 |  |  | LDXIM | \$02 | 2 MORE TIME UNITS |
| 17BD | 20 | C9 | 17 | AGAIN | JSR | TIMER |  |
| 17 CO | CA |  |  |  | DEX |  |  |
| 17 C 1 | D0 | FA |  |  | BNE | AGAIN | IF $\mathrm{X}=0$, THEN DONE |
| $17 \mathrm{C3}$ | 60 |  |  | OUT | RTS |  | OR ELSE DELAY MORE |
|  |  |  |  |  |  |  |  |
| 17 CL |  | 04 |  | WDSP | LDXIM | \$04 | WORDSPACE REQUIRES 4 MORE TIME UNITS |
| 17 C 6 | 4 C | BD | 17 |  | JMP | AGAIN | SO USE TIMER FOR THIS |
| $17 \mathrm{C9}$ | A5 | 00 |  | TIMER | LDAZ | TIME | GET TIME FROM ZERO PAGE |
| 17 CB | 8D | 07 | 17 |  | STA | TMER | LOAD DIVIDE BY 1024 TIMER |
| 17CE | 2 C | 07 | 17 | CHK | BIT | TMER | IS TIMER FINISHED? |
| 17D1 | 10 | FB |  |  | BPL | CHK | NO, WAIT FOR IT |
| 17D3 | 60 |  |  |  | RTS |  | YES, RETURN |

APPENDIX:
Using the KIM-1 Ports to Output the ASCII

Most readers will not have the same addressable video system used by the author. To use the receive portion of the program, some provision must be made to output the ASCII along with a strobe pulse. Below you will find a suggested program to do this. It makes use of ports SAD and SBD addresses 1740
and 1742 respectively. These are available on the application connector. The ASCII code appears at the KB COL A-G pins, while the strobe should appear at the TTY PTR pin.

NOTE: While this program should work it has not been tested.

## ALTERNATIVE ASCII OUTPUT

ORG \$03CA
** THIS ROUTINE HAS NOT BEEN TESTED **

| 03CA |  |  | ZTB | $\cdots$ | \$0000 |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 03CA |  |  | SAD | * | \$1740 |  |
| 03CA |  |  | SADD | * | \$1741 |  |
| 03CA |  |  | SBD | $\cdots$ | \$1742 |  |
| 03CA |  |  | SBDD | * | \$1743 |  |
| 03CA | A9 20 |  | CHAR | LDAIM | \$20 | ENABLE OUTPUT PULSE PINS |
| 03CC | 8D 42 | 17 |  | STA | SBD |  |
| 03CF | A9 21 |  |  | LDAIM | \$21 |  |
| 03D1 | 8D 43 | 17 |  | STA | SBDD |  |
| 03D4 | AD 40 | 17 |  | LDA | SAD | SAVE CONTENTS OF CURRENT |
| 03D7 | 48 |  |  | PHA |  | DISPLAY ON KIM-1 |
| 03D8 | AD 41 | 17 |  | LDA | SADD |  |
| 03DB | 48 |  |  | PHA |  |  |
| 03DC | B5 00 |  |  | LDAZX | ZTB | GET ASCII CODE |
| 03DE | 8D 40 | 17 |  | STA | SAD | OUTPUT ASCII |
| 03E1 | A9 FF |  |  | LDAIM | \$FF |  |
| 03E3 | 8D 41 | 17 |  | STA | SADD | ENABLE OUTPUT PORT |
| 03E6 | EE 42 | 17 |  | INC | SBD | STROBE PULSE WILL BE |
| 03E9 | EA |  |  | NOP |  | LENGTHEN PULSE |
| 03EA | CE 42 | 17 |  | DEC | SBD | NEGATIVE |
| O3ED | 68 |  |  | PLA |  | RESTORE SADD AND SAD |
| O3EE | 8D 41 | 17 |  | STA | SADD |  |
| 03F1 | 68 |  |  | PLA |  |  |
| 03F2 | 8D 40 | 17 |  | STA | SAD |  |
| 03F5 | A9 1E |  |  | LDAIM | \$1E | RESTORE SBDD AND SBD |
| 03F7 | 8D 43 | 17 |  | STA | SBDD |  |
| 03FA | A9 08 |  |  | LDAIM | \$08 |  |
| 03FC | 8D 42 | 17 |  | STA | SBD |  |
| 03FF | 60 |  |  | RTS |  |  |

## PET SOFTUARE FROM COMMODORE

Roy O'Brien
P.O. Box 187

Somerset, NJ 08873

It appears that in response to specific questions, Commodore is sending out selected Application Notes. The software consists of the following:

Machine Language Monitor - (9 pages) A discussion of the TIM program as adapted to the PET. Early PET owners are supposed to receive TIM on cassette and later PETs will have TIM in ROM.

PET Cassette Files - (31 pages)
A learn-by-doing mini-course in file management with the PET.

IEEE-488 Devices - (5 pages)
A listing of available equipment which directly interfaces to the PET. Gives device, model number, manufacturer; includes printers, counters, measurers, ADCs, DACs, timers, synthesizers, analyzers, plotters, tapes, discs, etc.

BASIC Bugs - ( 4 pages)
Kinks, quirks and bugs in PET BASIC.
PET and ASCII - (4 pages)
Definitions and symbol codes, including a neat little program which shows graphics and codes on screen.

PET Uses Its Memory - (1 page) A reprint of PET memory usage from PCCs Nov/Dec 1977 issue.

Animating Your PET - (2 pages)
How to use the programmable cursor controls to create moving graphics.

Some Questions and Answers - (11 pps) Things you always wanted to know and weren't afraid to ask; summarized. A must for PET owners.

## HIGH RESOLUTION GRAPHICS

In response to your requests, we now offer the K-1008, a Dot Matrix display board ( $320 \mathrm{H} \times$ 200 V for the KIM-I.
But - we didn't stop there. We also call it an 8 K memory board, directly connected to your KIM-I. Full read/write with no wait states or snow ever.
And - we made it low power to reduce system costs. In fact our 18 watt K-1000 power supply can typically power your KIM-1 plus 32K of K-1008 memory.
How to use it - The K-1008 visible memory only needs a power supply and a KIM-1 to function as memory. Add a standard monitor and you have high resolution graphics for diagrams, graphs, even variable font text up to 22 lines of 53 characters.
K-1008 Assembled/Tested
$\$ 289.00$ board $\$ 40.00$ Graphic Software Listing
$\$ 20.00$
K-1000 power supply $\$ 40.00$
K-1005 5 slot card file $\$ 69.00$
Micro Technology Unlimited
P.O. Box 4596

Manchester, N.H. 03103

FINALLY:


CGRS MICROTECH introouces a 6502 COMPUTER SYSTEM

- S10D STANDARO EUS COMPATIELE
- MRU CARO WITH EKRAM-AKROM ONBDARO
- T.I.M. (GSЗOJSYETEM I/O CARO
O.M.A. FRDNT CONTRQL PANEL
- INTROQUCTORY SYSTEM

KIT ASSEM
MPU CARS: 1 KRAM
FRONT PANEL:HEX OISPLAY $\$ 249.95 \quad \$ 299.95$

* STANDARO SYSTEM

MPUCARO: 1K RAM
TIMM IO CARO
T.M. IO CARO
SIOO MOTHERBOARO : $\mathbf{7 S L O T}$$\$ 349.95 \$ 449.95$ SOWER SUPPLY EV:IOA $\pm 18 \mathrm{~V}$ :1A

SEND CHECK OR MONEY OROER TD:
CGRS MICROTECH
CGRS MICROT
p.O. BOX 3EB
S.O. BOX 3BE

## EARLY PET-COHPATIBLE PHODUCTS

Charles Floto
325 Pennsylvania Ave., S.E. Washington, DC 20003

Throughout the five months I've had my PET, I've felt the biggest design oversight was leaving out a speaker. Commodore even went to the trouble of removing one, along with its amplifier, from the tape drive.

The versatility of the Apple II's audio output is nice, but I'd be satisfied with a simple beeper like the one in the Heath Company's H8. That's why I'm spending $\$ 19.95$ for the PETsqueak from HUH Electronic Music Productions (P.O. Box 259, Fairfax, CA 94930 415/4577598). This assembled and tested device doesn't just produce audible output under user control. It also beeps automatically during program loading or saving to indicate file headers and completion of the operation. I look forward to being able to turn away from my PET and still keep track of what's happening.

PET-compatible products from HUH scheduled for April and May delivery include an 8-bit digital-to-analog converter, an adapter for a video monitor (so you can have a larger screen facing a different direction), and an S-100 bus interface.

While I'm looking forward to adding the beeper to my PET, the thing that will really enhance its value is a compatible printer. The big news this month is that you can now hook any RS-232 printer to your PET. The necessary adapter is sold by Connecticut microComputer ( 150 Pocono Rd., Brookfield, CT 06804). Assembled and tested, but without power supplies, case, or RS232 connector, it goes for $\$ 103.50$ with shipping and handling. The complete version is $\$ 174$. The speed will be set at 300 baud unless another rate is requested at the time of ordering. This may be changed by the user later. With the PET ADApter model 1200 you can produce not only program listings, but
also mailing labels, letters, etc. The appearance will naturally depend on the printer used. Lower case letters are substituted for the graphics character.

The third addition I plan to make to my PET is a 6502 assembler written in BASIC. I ordered this for $\$ 24.95$ from Personal Software (P.0. Box 136-M3, Cambridge, MA 02138 617/783-0694).

While I'm content with the PET keyboard anyone who wants to hook up another one may be interested in the ASCII keyboard interface sold by Excel Co. ( 2241 Tamalpais Ave., El Cerrito, CA 94530 415/ 237-8114). Prices start at $\$ 65$.

The makers of the KIMSI have announced the PETSI. In kit form with one S-100 connector it's $\$ 105$. Assembled with the maximum of four S-100 slots it's $\$ 165$. Neither version includes a power supply. Forethought Products (P.O. Box 386-D, Coburg, OR 97401 503/485-8575) is the manufacturer.

May delivery is scheduled for an RS-232 interface from The Net Works (5014 Narragansett \#ó, San Diego, CA 92107 714/223-1176). Single port version is $\$ 240$; dual port $\$ 280$.

The PET Vet will have more to say about these and other PET oriented products in future issues of MICRO. If you have information about PET products, as a manufacturer, dealer, or user, please send materials to:

The PET Vet MICRO P.O. Box 3
S. Chelmsford, MA 01824

# TEE MICRO SOFTUARE CATALOG 

Mike Rowe<br>P.0. Box 3<br>S. Chelmsford, MA 01824

As a service to the 6502 community, MICRO will publish a continuing catalog of software available for 6502 based systems. The source of this information will normally be the authors or distributors of the software. Since there is only a limited amount of space which can be devoted to this effort, there will be some restrictions placed on what is published. To qualify for inclusion in the catalog the software must be currently available, should have been sold (or given) to at least twenty-five customers, must be of general interest, and must be significant. "Significant" means that the program is not just a short utility which could be presented as a one-page article in a magazine, or a simple game, etc. The intent of the catalog is not to promote everyone selling everything, but rather to highlight the important software packages which do exist.

Name: ASSM/TED
System: Preconfigured for TIM
Can be modified for other systems.
Memory: 4K RAM
Language: Assembler
Hardware: CRT and Keyboard, tapes and printer optional.
Description: A resident Assembler/Text Editor. Syntax very similar to MOS Technology. Produces relocatable object code on tape and can store directly executable code in memory during assembly. Programs can be assembled from memory of tape. Includes 17 operating commands and 16 pseudo ops. Editor has auto line numbering, file formating, and a manuscript feature.
Copies: Information not provided.
Price: \$25.00
Includes: Hex Dump of ASSM/TED and Relocating Loader, and Operators Manual. No tape provided.
Ordering Info: Specify memory limits: 0200-1200, 0400-1400, 1000-2000, or 2000-3000. Select one.
Author: C. W. Moser
Available from:
C. W. Moser

3239 Linda Drive
Winston-Salem, NC 27106

Publication of information about any software in this catalog does not imply anything about its worth, capabilities, documentation, etc. We depend on the information supplied to us. We will not knowingly include any software that is not worthy, and we reserve the right to publish additional information about these products - be it good or bad that we receive from our readers or any other valid source.

It is easy to get your package listed. Just write to the above address and provide the information required as shown in the listings below. Please write your own "description". If we have to write the description from general information you provide, we may miss points which you think are important and emphasize things you think are trivial. Also, material which is presented in the proper form will normally get priority over other material.

Name: COSMAC 1802 Simulator
System: KIM-1
Memory: Less than 1K RAM
Language: Assembler
Hardware: Basic KIM-1
Description: Permits the KIM-1 to simulate the COSMAC 1802 by executing its instruction set. The simulator does this by interpretting the COSMAC instructions in a normal program sequence and making all internal COSMAC registers available for examination at any time. They may be viewed statically in a single step mode or dynamically in a trace mode. All COSMAC software features are supported with the exception of DMA.
Copies: Just released. Will be discussed in an article in Kilobaud.
Price: $\$ 10.00$
Includes: KIM-1 cassette tape, user manual, and complete source listing.
Ordering Info: None required
Author: Dann McCreary
Available from:
Dann McCreary 4758 Mansfield St, \#2M San Diego, CA 92116

Name: PLEASE
System: Basic KIM-1
Memory: Basic KIM-1 memory
Language: Assembler/PLEASE
Hardware: Basic KIM-1
Description: A collection of games and demos. Includes a 24 hour clock, HiLo game, Mastermind, Shooting Stars, Drunk Test, Reaction Time Tester, Adding Machine, and more. Written in a "highlevel" language - PLEASE. Permits the user to modify and create his own programs. Let's you show off your KIM-1, and teaches you how to use it.
Copies: Over 800 have been sold
Price: $\$ 15.00$
Includes: Operators manual, complete source listings, PLEASE language description, with object code on Hypertape.
Ordering Info: None
Author: Robert M. Tripp
Available from:
The COMPUTERIST
P.O. Box 3
S. Chelmsford, MA 01824

Name: Micro-ADE
System: KIM-1 (easily modified for use with other 6502 based systems)
Memory: 8 K RAM or 4 K EPROM +4 K RAM
Language: Assembler
Hardware: Terminal - CRT or TTY, cassette units optional
Description: A combination Assembler, Editor, and Disassembler. Uses MICRO 6502 syntax. With automatic cassette controls, any length file may be edited and assembled. Object files may be automatically dumped to cassette and for short programs may be dumped to and executed from memory. Includes many useful commands for handling cassettes, moving data in memory, and so forth. Copies: Hundreds
Price: $\$ 25.00$ without source listings $\$ 25.00$ for source listings
Includes: Extensive user manual which
includes source listings for the $1 / 0$
to permit user modification. Object
on Hypertape cassette.
Ordering Info: Specify with or without the optional source listings.
Author: Peter Jennings
Available from:
Micro-Ware Ltd.
27 Firstbrooke Road
Toronto, Ontario
Canada M4E 2L2

Name: The 6502 Program Exchange
System: TIM and KIM-1
Memory: Depends on Program
Language: Assmebler, BASIC, FOCAL
Hardware: Depends on Program
Description: A large collection of programs for 6502 based systems. These include utilities, games, subroutines, an assembler, editor, and a high level language: FOCAL.
Copies: Few to Many depending on the particular program.
Price: Depends on program. Many are based purely on number of pages of code. Major packages are priced separately.
Includes: Normally includes source listings, documentation, sheets of sample run, and paper tape. KIM-1 cassettes at no additional charge if user supplies cassettes.
Ordering Info: Write for catalog.
Author: Many different authors.
Available from: The 6502 Program Exchange 2920 Moana Reno, NV 89509

Name: Personal Savings Investment Loan Repayment Direct Reduction Loan Info.
System: APPLE II
Memory: At least 16 K
Language: APPLESOFT BASIC
Hardware: Standard APPLE II
Description: Three separate programs. PSI - compute future value of your investments; monthly amount needed to get to a certain goal at a certain time. LP - determine monthly payments for a car, house or other type of load.
DRLI - find the total interest paid and remaining balance is for a loan.
Copies: Over 25 combined
Price: $\$ 3.75$ (including handling) each of the three programs.
Includes: Object on cassette tape. A listing of the program and examples of program useage.
Ordering Info: Specify which program. Author: Les Stubbs
Available from: Les Stubbs 23725 Oakheath Place Harbor City, CA 90710

The COMPUTERIST
P.O. Box 3
S. Chelmsford, MA 01824

Name: TINY BASIC
System: KIM, TIM, Jolt, Apple I
Memory: Minimum of 3 K
Language: Assembler
Hardware: User defines I/O
Description: TINY BASIC is a subset of regular BASIC, limited to 16-bit integer arithmetic [+, -, *, /, ()]. There are 26 variables ( $\mathrm{A}-\mathrm{Z}$ ), no stirngs and no arrays. The following commands are functional: LET PRINT INPUT IF-THEN GOTO GOSUB RUN LIST CLEAR RETURN REM END. TINY BASIC does not contain any I/O instructions; three JMPs link TINY to the user's I/O routines. These are well documented in the manual.
Copies: "Several hundred 6502 version" Price: $\$ 5.00$
Includes: 26 page User Manual and a paper tape in standard hex loader format. Hex Dump may be substituted upon request for paper tape.
Ordering Info: Specify version: TB650K (0200-0AFF) KIM, TIM, .... TB650J (1000-18ff) Jolt TB650T (2000-28FF) KIM with 4K RAM
Author: Tom Pittman
Available from:
ITTY BITTY COMPUTERS
P.O. Box 23189

San Jose, CA 95153

Name: HELP Mailing List Package
System: Basic KIM-1
Memory: Basic KIM-1
Language: Assembler/HELP
Hardware: Terminal, Cassettes, Relays Description: A complete package for creating, maintaining, and printing mailing list information. A high speed cassette routine reads/writes at 800 baud (twelve times the KIM-1 rate) and can store about 900 names on one side of a 60 minute tape. Selective printing of mailing list. This package is used to maintain the MICRO mailing list This package is written in HELP, a "high-level" language which makes it easy to customize the package for your own requirements.
Copies: Over 100
Price: $\$ 15.00$
Includes: An extensive user manual, a detailed discussion of the HELP language, and complete source listings. Object on Hypertape.
Ordering Info: None
Author: Robert M. Tripp
Available from:
The COMPUTERIST
P.O. Box 3
S. Chelmsford, MA 01824

Name: ASM/TED
System: KIM-1 (may be modified for use with other 6502 based systems)
Memory: 6K RAM
Language: Assembler
Hardware: TTY
Description: The text editor performs line editing in RAM and can dump/load to paper tape or audio cassette. The resident assembler is single-pass using the standard MOS Technology syntax. Source code may be paper tape or memory resident and object code is always to memory.
Copies: Information not provided.
Price: $\$ 70.00$
Includes: 50 page manual, source listings, and object on KIM cassette or paper tape.
Ordering Info: Send $\$ 2.00$ for current catalog of available software.
Author: Not specified
Available from:
ARESCO
450 Forest Ave., Q-203
Norristown, PA 19401

## Name: MicroChess

System: Basic KIM-1
Memory: Basic KIM-1
Language: Assembler
Hardware: Basic KIM-1
Description: Plays a reasonably good game of chess on a basic KIM-1. Has programmed openings. User enters his move via the KIM keypad and the KIM Display shows the move. The computer then makes its move and displays it. Program may be set to play at different speeds: 3, 10 , or 100 seconds per move average. A great way to demo your KIM. Copies: Hundreds
Price: $\$ 10.00$ without cassette $\$ 15.00$ with cassette
Includes: Operator's manual, source listings, and a detailed discussion of the operation of the program. Object on cassette tape optional.
Ordering Info: Specify tape or not.
Author: Peter Jennings
Available from:
Micro-Ware Ltd.
27 Firstbrooke Road
Toronto, Ontario
Canada, M4E 2L2

## The COMPUTERIST

P.O. Box 3
S. Chelmsford, MA 01824

## Three PLUSes for the KIM－1：



## 

$\$ 30^{00}$
Made by＂The Enclosures Group＂especially for the KIM－1／MEMORY PLUS combination．The MEMORY PLUS is mounted directly below the KIM－1 providing a compact package about 2．5＂high which affords your system a high degree of protection from damage，dust，curious fingers，etc．

## 回回㞓员 回US ${ }^{\text {TM }}$

Designed specifically for the $K I M-1$ ．It has regulated $+5 V$ and $+12 V$ for the KIM－1 and more than enough unregulated +8 V to power the MEMORY PLUS． It is completely enclosed in a black bakelite case measuring about 6．8＂ by $5.6^{\prime \prime}$ by $3^{\prime \prime}$ ．It is fully assembled and tested and weighs about 3 lbs．

MEMORY PLUS is $\$ 245$ with everything except EPROMs． KIM－1／MEMORY PLUS Cables are $\$ 10.00$ Includes 60 page manual，cassette tape，connectors．

P．O．Box 3
S Chelmsford，MA 01824
617／256－3649

# APPLE II PRIMTIEG UPDATE 

C. R. (Chuck) Carpenter W5USJ<br>2228 Montclair Place<br>Carrollton, TX 75006

"Printing with the Apple II" [MICRO \#3] included information that has been revised. Since the article was written, I've improved some things and I'd like to pass them along.

## The Adapter Didn't

After using the adapter circuit for a couple of months, I took a good look at what was happening. The conclusion was nothing! Initially, it didn't work when I connected it to the RS-232 receiver on the PS-40. I connected it to the serial TTL input (pin A7) and it worked. The voltage swing wasn't excessive (clamped with some diodes), so I left it hooked-up. Should have been a clue. But at the time I didn't see it, and anyway, it worked.

During one of our (infrequent) snowedin days here in Texas, I had time to think about it. There wasn't any apparent reason not to hook it up directly; and I did. It worked the way it should so I had a no-interface-required computer to printer system. When I received my new Apple Operator's Manual I noticed a new interface circuit, not the one I used as originally provided.

All that is needed is to connect a signal lead and ground from the Apple to the printer. The signal lead connects to Pin 15 of Apple's game paddle connector. Also to Pin A7, TTL serial data in, on the printer. I soldered the game paddle connector to the 16 pin header. No other connections needed.

## Now You Can Start and Stop

Ted Spradley, a programmer/engineer at work, helped me with the machine language print program. His analysis suggested restoring the page zero registers to make the print routine stop. As you more experienced programmers would know, it worked. I rewrote the program to store and restore the page zero data and now the routine turns on and off under program control. The program, shown in Figure 1, was a revelation to me. Again, my thanks to Ted for his assistance.

The Blues Are Gone
Most of my programs are printed on the paper that turns blue (and fades). Telpar has a black on off-white paper now. This new paper makes a much sharper copy too. The blue paper was also susceptible to smearing. This did not help the copy quality either, photographically or Xerographically.

There! Now that the problems are resolved, what's holding you back? Let's get printing.

Author's Note: Even if you don't have a printer, the print routine is useful. Use it to slow the screen speed down. This way you can read a listing during a slow scroll.

## Getting Decimal Values From Hex Data

For some other program, POKE was used to enter machine language from BASIC. I did this for the print routine. All the HEX values have to be converted to decimal. At first I did this with the TI Programmer. Then I "discovered" what PEEK is all about. A BASIC program to print the decimal values simplifies the job. Convert the first and last addresses (to do a range of addresses) to their decimal values These values are 875 and 967 for the print program. Then use them in a FORNEXT routine like this:

```
100 FOR I=875 TO 967:PRINT PEEK(I);: PRINT" ";:NEXT I:END
```

This reduced a two hour job to about ten minutes. Hooray for progress.

Listing
9GELLL

| EEE－ | FE PE | LIMF | \＃6 |
| :---: | :---: | :---: | :---: |
| EgI－ | EL E6 93 | ETH | aboce |
| ET6－ | H5 | LIIH | 者\％ |
| ETE－ | EIi Cl | STF | ange |
| 日6： | HG 0 | LIIH | \＃き\％ |
| G67－ | 5 E | STH | F\％ |
| ET－ | 968 | LIIF | \＃もけ |
| ETE－ | E5 | STH | 韦7 |
| Gril－ | 6 | FTS |  |
| GTE－ | Fil Le be | LIIH | 中日GG |
| ES1－ | 5 E | ETH | F\％ |
| 698－ | Fill CO F | Lif | \＃ロ\％\％ |
| ESE－ | ET | STA | $\ddagger \%^{\circ}$ |
| bes－ | 0 | FTS |  |
| 685－ | 848 | $8 \mathrm{~T}^{1}$ | \＄5 |
| E6S－ | 45 | FHFI |  |
| E6E－ | E6 HE ES | 16F | ま＠G\％ |
| Cor－ | 6 | FLF |  |
| Q90－ | Se EI | $\mathrm{C}+\mathrm{FF}$ | \＃き， |
| E6E－ | Live Eic | EtE | まロー4 |
| E694－ | FO | LIIH | \＃：\％ C |
| 696－ | ESHES | IEF | \＃ロ\％ 5 |
| E09－ | Fers | LIH | \＃\＃ |
| ESEE－ | DE FIC | IEF | \＃FEHE |
| E89E－ | FG OII | LIIH | \＃も时 |
| ETEP－ | F4 3 | LIH＇ | 伟5 |
| ESPE－ | 4 COEFF | INT | 抽IFE |
| GTEF－ | Fer EE | Liry | \＃\＃もE |
| E6F－ | 1 c | CLE |  |
| Etie－ | 46 | FHF |  |
| ESF－ | EGE | ES | \＃E6E6 |
| ESEP－ | Fil 56 | LIIH | \＃565 |
| ECHE－ | 96 | ET： | \＃6E\％ |
| EST－ | FII 59［el | LIIF |  |
| WEE－ | H9 IS | LIIF | \＃\＃ IS |
| ESE－ | 45 | FHH |  |
| COEE－ | Fis 2 C | LIIH | \＃\＃ご |
| CEE－ | 4 H | LEF |  |
| CEO－ | FI | ECT | \＃EEE |
| EEE－ | 6 | FLA |  |
| EEEC－ | E9 1 | EEC： | \＃き61 |
| ESEE－ | Lat 5 | EFPE | \＃6Es |
| E00－ | $\theta$ | FLH |  |
| 601－ | En | FOF： |  |
| 60\％ | 3 | TE＇${ }^{\text {＇}}$ |  |
| 60－ | IV ES | Erle | teres |
| E65－ | ET | FTS |  |
| 86C－ | FQ Fir | EED | ま65 |

＋GEE． 3 F
EGE－AE GE EII CE ES

$$
\begin{aligned}
& \text { EEG- HII } 59 \text { Ag IG } 48 \text { BG EG }
\end{aligned}
$$

$$
\begin{aligned}
& \text { : } \ddagger
\end{aligned}
$$

START Print STOP Print
＊36BG＊37EG
＞CALL $875>$ CALL 894 ］SP＝USR（875）JEP＝USR（894）

Type in one of above and then type RETURN to activate the command．
＊$=$ from Apple Monitor
$>=$ from Integer BASIC
］＝from Applesoft BASIC
Change 03B4 to 4D for 300 baud．

Figure 1
Listing and HEX Dump of Machine Language Print Routine


## MICRO STUFF

## Mailing Labels

Barring unforseen difficulties (last May we lost electricity for four days due to a snow storm), the mailing label on your copy of MICRO will have been generated on a KIM-1 with a Diablo type printer and the HELP Mailing List Package. Note near your name the two or three characters. The first two digits indicate the last issue you are scheduled to receive under your current subscription: $06=$ issue number 6 . The third character has particular meaning:
$\mathrm{X}=$ your name will appear on any mailing lists we sell, unless you notify us to remove it;
any other letter indicates you are getting MICRO free as an advertiser, exchange, or something;
no letter indicates that your name will not be included in mailing lists we sell, per your request.

Our New Printer
This issue of MICRO is being printed by a new printing company. We anticipate that the quality will be as good as the previous work.

## Deadlines

With our new printer (he's cheaper but takes longer), deadlines are even more important than before. All ADs must be received by May 14 for the June/July issue. Articles should be received as soon as possible.

## Calendar/Directory

If enough information is provided to make it worthwhile, we can publish a regular Calendar of 6502 related events and a Directory of 6502 Clubs. Since MICRO is only published every other month, remember to give information for several months at a time.

## KIM-1

## \$245

## SPECIAL - includes Power Supply

MEMORY PLUS 8K RAM for KIM \$245

- with 2716 EPROM sockets and programmer
- 6522 VIA (includes 2-8 bit ports and 2 timers)

SPECIAL - includes edge connectors and cable for direct KIM connection

## PROBLEM SOLVER SYSTEMS KM8B

- 8K low power static RAM, completely socketed
- factory assembled and tested
- completely compatible with KIM-4 motherboard

KIM - 4 MOTHERBOARD
Power Supply for KIM (KL512)
$+5 \mathrm{~V},+12 \mathrm{~V}$ regulated
$+8 \mathrm{~V},+16 \mathrm{~V}$ unregulated
plenty of power for KIM-1 and 8 K memory
First Book of KIM
PLEASE.games and demo package on cassette $\$ 15$
MICROCHESS - runs in IK RAM
AB Computers
P.O. Box 104, Perkasie, PA 18944

## MICROBES

Tiny Bugs in Previous MICROs
EMPLOYING THE KIM-1 AS A TIMER ....
3:5 020E should be A9 99 LDAIM $\$ 99$ since the processor is in decimal mode, not binary.

3:7 02A6 should be E4 03 not E0 03.
LIGHTING THE KIM-1 DISPLAY
Back cover There is no need to add Hex 80 to the sum of the individual LED segments to control PAT. It does hurt, but it is not required.

## STAEDARD 6502 ASSEMBEY SYETAE?

Hal Chamberlin
29 Mead Street
Manchester, NH 03104

I could not help noticing the comment about MOS Technology's assembler syntax for the 6502 in MICRO \#2. Judging from the force of that comment and the fact that every 6502 program I have seen uses a different assembler and systax there must be a great deal of discontent with MOS Technology's syntax.

Consideration of the history of 6502 development is all that is necessary to explain most of the features of its assembler syntax. The designers initially worked at Motorola with the goal of incorporating leading features of the PDP-11 instruction set into the 6800. Later, after leaving Motorola and designing the 6502 for MOS Technology, their PDP-11 experience served as a model for an assembler syntax to adequately handle the 13 addressing modes and other features of their creation. The result is the syntax described in about 10 square inches on the 6502 card and illustrated by the KIM assembly listings we all practically know by heart. The PDP-11 is one of the most used minicomputers ever and I have not heard of any significant group of '11 users abandoning DEC's syntax even though it can become a little cryptic.

So let us take a close look at the MOS Technology syntax, iterate what is right about it, and see how we can live with those features that are less than ideal. Note that $I$ am not at all against extensions of what they have defined but $I$ think it is important that an assembler be able to correctly assemble the KIM source as printed.

First we have the assembler directives and other statements that have nothing to do with the instruction set. For the most part these have been lifted directly from the PDP-11 assembler manual. The distinguishing feature about these statements is that they are preceeded by a period. I see nothing particularly wrong with these except perhaps that some of them are longer than three characters meaning that an opcode scanner might have to be a little more sophisticated than it would otherwise be. One definite problem though is the
method that must be used to reserve areas of memory for data storage. I prefer the "DS 5" form rather than the ". =.+5" form for reserving five bytes probably because of an IBM background. But the real problem is that unless the assembler is carefully written, the location counter value printed to the left of such a statement gives the address of the first byte of memory used in the next statement rather than the address of the first byte of memory reserved in this one. However I think that the latter form can be lived with if one realizes that the expression ". =.+" is really the same as "DS" and provided the assembler prints the right address.

Now what about the machine instructions themselves? A tendency noted in several homebrew assemblers is to give every addressing mode variation of every instruction a different mnemonic. Although this is a good advertising ploy to swell the 57 listed op codes into 151 "variations", it does not make good sense. The operation code should merely specify the operation and the operand column should specify the operands. In my way of thinking the addressing mode is part of the operand (it tells where the operand is) and not the operation. Of course MOS Technology violated this somewhat by putting the register designation in the op code but that is not nearly as bad as putting everything in the op code.

One particularly nice feature of the existing syntax is the specification of the two indirect addressing modes. The designation "(SYMB,X)" clearly indicates that the value of SYMB is added to $X$ before looking in the base page for the effective address and the designation "(SYMB), Y" says that the indirect cycle occurs before the contents of $Y$ are added in to form the effective address. There should never be any problem with the use of parentheses for indicating indirect and the use of parentheses in arithmetic expressions. It is unfortunate however that indexed addressing is of the form "SYMB,X" rather than $\operatorname{SYMB}(X) "$ as on most other systems but it can certainly be lived with.

With respect to the other addressing modes, the assembler should take care of determining whether the "zero page" form or the "absolute" form is to be used. Essentially the assembler would look at the value of the address and if it is less than 0100 (hex), use the appropriate zero page addressing form of the instruction. Besides always insuring the shortest possible program (both space and time), it frees the programmer from learning many of the addressing mode restrictions of certain instructions. The assembler will flag an error only when it is physically impossible to perform the requested operation.

One last minor gripe is the field separators (colon after symbols and semicolon before comments) required which adds (slightly) to typing effort and uses three valuable print column positions. Of course this is also straight out of the PDP-11 assembler. I know a powerful assembler can be written without this requirement and still have free format (IBM 360 assembler) but my programmer friends say that explicit
delimiters can have important advantages. Anyway I live with it.

I can hear the cries now of "Sure it makes sense but it is so complicated to write a syntax analyzer for it". Of course our cross-town rivals (8080, Z-80) are already well into macro assemblers and linking relocating loaders and we are still working out the assembler syntax for our baby! If we believe that ours is a more powerful computer, surely an assembler with automatic address mode selection and conformance to our own manufacturer's assembly language is not too difficult a task to handle.

Editor's Note: While I do not want to use too much space in MICRO for debates on matters of personal preference, I will make space available in the next issue of MICRO for a rebuttal by a proponent of an alternative syntax. If no one writes such a rebuttal, I will do it myself, but I would much prefer to hear from one of you.

## A WORM II THE APPLE?

Mike Rowe
P.O. Box 3
S. Chelmsford, MA 01824

There may be a serious problem hidden deep within the Apple II according to John Conway and Jack Hemenway of EDN magazine. As part of their system design project based on a bare-board Apple - "Project Indecomp" - they tried to interface a 6820 PIA to the Apple, and uncovered a potentially serious problem. The normal way to operate a 6502 based system is to provide an external clock [phase 0] to the 6502 which then generates two non-overlapping clock signal [phase 1 and phase 2] which are used to control all system timing. For some reason, the design of the Apple II violated this basic clock scheme and uses the phase 0 external clock instead of the 6502 generated phase 2 clock. While these two clocks
are very similar, they are not identical. Phase 1 and phase 0 have an overlap of about 50 nanoseconds. For many parts of the system this is not important, as indicated by the fact that the Apple II works. For other devices, however, such as the 6820 PIA, this difference is critical to the extent that the device simply will not work. A report in EDN scheduled for 20 May will cover this problem in detail, and we will. try to get more info for the next issue of MICRO. Is the problem serious? Critical? Fatal? It is probably too early to judge the effect of this problem. It may not have an adverse effect in many systems. It may be possible to correct. Or it may be a very serious system problem.

## 핑ITITG FOR MICRO

One of the reasons I like the 6502 is that it seems to attract a lot of very interesting, active, enthusiastic users. I spend several hours on the phone each week talking to people who are so excited about what they are doing with their system that they just have to talk to someone. Oh, sometimes they pretend they have some "burning" question or want to order some small item, but really they mostly want to tell someone about all of the fun they are having or the discoveries they are making.

While I enjoy these conversations, and consider them one of the "fringe benefits " of editing MICRO, it disturbs me that many of these enthusiasts who are willing to spend five to ten dollars on a phone call to me, are not willing to spend a little time writing down their
information for publication in MICRO where thousands can share it (and they can earn a few dollars).

MICRO, in order to serve its main purpose of presenting information about all aspects of the 6502 world, needs to receive information from a wide variety of sources, To achieve a more balanced content, we desperately need articles on: industrial, educational, business, home, and other real applications of systems; non-KIM, -Apple, -PET systems, homebrew and commercial; techniques for programming, interfacing, and expanding systems; and many other topics. Look to your own experience. If you have anything to share, then take the time to write it down. The "Manuscript Cover Sheet" on the next page should serve as a guide and make it a little easier to submit your article.

## Power Supply for KIM \$37

## KL Model 512 <br> Total Capacity 4.3 amps

+5 volts regulated to 1.4 amp
+12 volts regulated to 1.0 amp
+8 volts to 4.3 amp
+16 volts to 1.0 amp

## COMPLETELY ASSEMBLED

Power for KIM-1 and 8K memory
Fused primary
Current limit and thermal overload protection for regulated outputs
Enclosed in case with rubber feet Includes line cord and connector cable

DEALER AND QUANTITY PRICES AVAILABLE

## MICRO SUBSCRIPTIOIS

MICRO is published bi-monthly, six issues per year. Single copy price is $\$ 1.50$. Subscriptions are available at the following rates:

Surface Mail: All Countries $\$ 6.00$
Air Mail:

$$
\begin{array}{ll}
\text { Central America } & \$ 12.00 \\
\text { Europe \& South America } & \$ 14.00 \\
\text { Other Countries } & \$ 16.00
\end{array}
$$

All subscriptions start with the NEXT issue after receipt of your order.

Back issues are available, while they last, at $\$ 1.50$ per copy (plus $\$ 1.25$ for air mail postage overseas or $\$ .50$ for surface postage overseas).

## KL POWER SUPPLIES

P.O. Box 86

Montgomeryville, PA. 18936

The COMPUTERIST
P.O. Box 3

S Chelmsford, MA 10824

## MANUSCRIPT COVER SHEET

Please complete all information requested on this cover sheet.
Date Submitted:
Proposed Title:
Author(s) Name(s):

Mailing Address:
(This will be published.)

Area Code: $\qquad$ Phone:
(This will NOT be published.)
AUTHOR'S DECLARATION OF OWNERSHIP OF MANUSCRIPT RIGHTS: This manuscript is my/our original work and is not currently owned or being considered for publication by another publisher and has not been previously published in whole or in part in any other publication. I/we have written permission from the legal owner(s) to use any illustrations, photographs, or other source material appearing in this manuscript which is not my/our property. If required, the manuscript has been cleared for publication by my/our employer(s). Note any exceptions to the above (such as material has been published in a club newsletter but you still retain ownership) here:

Signature(s):

Date:
Any material which you are paid for by The COMPUTERIST, whether or not it is published in MICRO, becomes the exclusive property of The COMPUTERIST, with all rights reserved.

## A Few Suggestions

All text material will be retyped. Therefore your format does not matter as long as it is readable. Double spaced, typed, is preferable, but not required. Any figures should be neatly drawn to scale as they will appear in MICRO. If we have to redraw the figures and diagrams, then we normally will pay less for that page. Photographs should be glossy prints either the same size as the final will be or twice the final size. We will re-assemble all programs to obtain clean listings using the syntax we have adopted (see inside back cover - MICRO \#1). Since others will be copying your code, please try to thoroughly test it and make sure it is as error free as possible. Submit your articles early. We will try to get a proof back to you for final correction, but with our tight schedule this may not always be possible. Send your manuscripts to:

Robert M. Tripp, Editor, MICRO, P.O. Box 3, So. Chelmsford, MA 01824, U.S.A.

# 6502 BIBLIOGRAPHY PART III 

William Dial<br>438 Roslyn Avenue<br>Akron, OH 44320

180. Gordon, H.T., "Decoding 650X Opcodes", Dr. Dobbs Journal 2, No. 7, pp 20-22 (Aug. 1977)

Subroutines that can be used with KIM.
181. Butterfield, Jim F., "A High-Speed Memory Test Program for the 6502" DDJ 2, No. 7, p 23 (Aug. 1977)

A memory test program written for the KIM system.
182. Anon. "Ohio Scientific's New Disc Operating System", DDJ 2, No. 7, p 32 (Aug. 1977)

The OS-65D is a complete operating system for all disc based OSI computer systems. Includes DOS, 8 K Basic, Assembler, Editor, Extended Debugger and a Disassembler.
183. Anon.., "OSI offers Computer that thinks in Basic for $\$ 298$ ", DDJ 2, No. 7, p 39 (Aug. 1977)

OSI's new Model 500 CPU board can be used as a stand-alone computer or as the PCU in a larger system.
184. Moser, Carl W., 3239 Linda Dr., Winston-Salem, NC 27106, DDJ 2, No. 8, p 28 (Sept. 1977)

Announcement of New Product: $\$ 25$ for 6502 Editor and Assembler Hex Listing and Manual. Configured for TIM Systems.
Anon.,"lK Corner", OSI Small Systems Journal 1, No. 4, p 3 (Oct. 1977) Hex address and offset calculator program resides at ODDE to OEE4.
186. Anon., "Now You Can Play Star Wars", OSI Small Systems Journal 1, No. 4, pp 11-13, (Oct. 1977)

Star Wars program by Robert L. Coppedge requires 8 K Basic, OSI 440 Video Board and at least 4 K of RAM.
187. Anon., "Conventional Typewriter", OSI Small Systems Journal 1, No. 4 pp 8-9 (Oct. 1977)

Gary Smith's program for using the OSI-65V when interfaced to a printer to be used as a conventional typewriter and also modify the text for a data file.
188. Gordon, H.T., "OPLEGL Correction and a 6502 Scanning-Debugger", DDJ 2, No. 9, pp 42-44 (Oct. 1977)

Gordon offers a corrected version of his 650X subroutine, OPLEGL, and gives a new byte-count subroutine, NUMBYT. A new scanning-debugger, SIMBUG, is submitted.
189. Swope, J., "6502 Goodies", DDJ 2, No. 9, Issue 19, p 45 (Oct. 1977) Swope, President of CGRS Microtech, PO Box 368, Southampton, PA 18966, announces that his company has finished a 6502 computer board for the S100 bus.
190. Wozniak, Stephen, "Sweet 16: The 6502 Dream Machine", Byte 2, No. 11, pp 150-159 (Nov. 1977)

Sweet 16 is a 16 bit "metaprocessor" in software, intended as a 6502 enhancement package, not a stand-alone processor.
191. Shattuck, Bob and Schmidt, Bill, "Receive CW with a KIM-1", 73 Magazine, No. 206, pp 100-104 (Nov. 1977)

A program for receiving CW with optional TTY or KIM display.
192. Johnson, Donald J., "KIM-1 Sidereal/Solar Clock Correction", Interface Age 2, No 12, p 9 (Nov. 1977)

A correction in the listing given in the August issue of Interface Age permits 24 -hour operation.
193. KL Power Supplies, PO Box 86, Montgomeryville, PA 18936, Interface Age 2, N No. 12, p 140 (Nov. 1977)

The Model $512,4.5 \mathrm{amp}$. power supply is designed for KIM-1.
194. Micro Technology Unlimited, Box 4596, Manchester, NH 03108, Interface Age 2, No. 12, p 140 (Nov. 1977)

The MTU Model K-1000 power supply is designed to power the KIM-1.
195. Wasson, Philip A., "Trace", KIM-1/6502 User Notes, Issue 7/8, pp 2-3 (Sept \& Nov 1977)

With this program and. about $\$ 2.00$ worth of hardware you can see displayed on an oscilloscope screen, all of the registers in the 6502 and three consecutive memory locations.
196. Ohsiek, Charles C., "ID on Audio Cassette for SUPERTAPE", User Notes, Issue 7/8, p 4 (Sept \& Nov 1977)

Program allows writing an ID on the audio cassette tape prefixing the data SUPERTAPE writes out.
197. Hawkins, George W., "2-Task Alternating Scheduler Routine", User Notes, Issue 7/8, p 5 (Sept \& Nov 1977) Program allows two programs to be run together in the KIM-1.
198. Gordon, Hal, "A Catalog of KIM-1 ROM Bytes", User Notes, Issue 7/8, p 5, (Sept. \& Nov. 1977)

A table of the location of ROM bytes.
199. Anway, Allen, "Program BRANCH", User Notes, Issue 7/8, p 6 (Sept \& Nov 1977) With this program you can go through your program, find the Branch instructions and force the branch to see where you will end up.
200. Pollock, Jim, "KIM-1 to S-100 Bus Adapter", User Notes, Issue 7/8, p 7 (Sept. \& Nov. 1977)

This adapter allows KIM-1 to be used with S-100 boards such as the $\$ 125$ 8 K RAM board of Ithaca Audio.
201. Heinz, Harvey, "A Simple Music Program for KIM", User Notes, Issue 7/8, pp 8-9 (Sept. \& Nov. 1977)

This is an excellent tutorial program with basic level explanations.
202. Hapgood, Will, "An A/D Converter", User Notes, Issue 7/8, pp 10-11, (Sept. \& Nov. 1977)

A circuit for making very accurate A/D conversions using a Motorola dual-slope conversion chip, MC 1405 or 1505.
203. Butterfield, Jim, "KIM Blackjack", User Notes, Issue 7/8, pp 11-13, (Sept. \& Nov. 1977)

Game uses the KIM display to good advantage in this program.
204. Strandtoft, B., "KIM-1 Resident Programs and Subroutines", User Notes, Issue 7/8, p 14 (Sept. \& Nov. 1977) A list of KIM Monitor routines with brief explanations.
205. Goenner, Markus P., "TTY Rapid Load", User Notes, Issue 7/8, p 15, (Sept. \& Nov. 1977) Program starts at 0000 and is fully relocatable.
206. Parson, Charles H., "Read Temperature Once per Minute", User Notes, Issue 7/8, pp 16-18, (Sept. \& Nov. 1977) Program for temperature control systems.
207. Oliver, John and Hall, Williamson, "A KIM-1 Binary Dump and Load Routine", User Notes, Issue 7/8, pp 19-20, ( Sept. \& Nov. 1977) SUPERDUMP/SUPERLOAD allows the use of the KIM-1 Cassette tape interface to read and write data blocks under program control. 1 K bytes are dumped or loaded in less than 12 seconds.
208. The COMPUTERIST, PO Box 3, S Chelmsford, MA 01824, "MEMORY PLUS for KIM-1", New Product Announcement, MICRO, No. 2, p 2 (Dec 1977-Jan 1978)

New board for fitting directly beneath the KIM-1 has 8 K RAM, 8 K EPROM MOS Technology Versatile Interface Adapter, EPROM programmer, On Board Voltage Regulators; fully assembled and tested $\$ 245$; Intel 2716 2K EPROMS extra $\$ 50$ each.
209. Cole, Phyllis, "PET Update", Peoples Computers 6, No. 3, pp 6-7 (Nov-Dec 1977) Several rumors on the PET are answered.
210. Cole, Phyllis, "Our PET's First Steps", Peoples Computers 6, No. 3, pp 8-10, (Nov-Dec 1977)

An account of bringing a PET on stream in spite of a few initial bugs and limited documentation at the time.
211. Inman, Don, "The Data Handler Users Manual: Part 6", Peoples Computers 6, No. 3, pp 11-15, 44 (Nov-Dec 1977)

The latest contribution in this series covers multiplication and division programs.
212. The 6502 Program Exchange, 2920 Moana, Reno, NV 89509, "Software Announcement:, On Line 2, No. 15, p 7 (Nov. 16, 1977)

Recent software includes an extended version of FOCAL, a 4 K resident assembler and an efficient Mini-Editor.
213. MSS, Inc., "65XX Programs Available", New product announcement, On Line 2, No. 17, p 2 (Dec. 30, 1977)

Programs available include Disassembler, Loader, Punch, Dump, Memory Editor, Life Game, File Commands, Assembler/Text Editor, etc., MSS, Inc., 3201 East Pioneer Parkway, Suite 40, Arlington, Texas 76010.
214. Rychlewski, Walter J., III, "PET Demonstration Tape", On Line 2, No. 17, p 7, (Dec. 30, 1977), New Product Announcement.

Ten BASIC programs demonstrate most of the features of the PET; includes graphics and real time clock; $\$ 10$ cassette. 603 Spruce St., Liberty, MO 64068.
215. Purser, Robert Elliott, PO Box 446, El Dorado, CA 95623, On Line 2, No. 17, p 9 (Dec. 30, 1977), New Product Announcement.

PET layout sheet with SASE, free.
216. Anon, "1K Corner: Cassette Loader and Memory Block Transfer", OSI Small Systems Journal 1, No. 5, p 3 (Nov. 1977)

With this program the user may record his own programs via the 430B Super I/O Board in a format that is recognizable to the auto-load function in the 65 V Monitor PROM.
217. Anon, "Two New Software Packages", OSI Small Systems Journal 1, No. 5, pp 4-7 (Nov. 1977)

OSI has released two major new Disc software packages, Word Processor and 9-Digit BASIC which run under OS-65D version 2.0
218. Anon, "Two New Video Games", OSI Small Systems Journal 1, No. 5, pp 8-12 (Nov. 1977)

SAM (Surface-to-Air Missile) and BOMBER require OSI 8K BASIC, OSI 440 Video Board, terminal and Keyboard, and at least 4 K of RAM.
219. Pfeiffer, Erich A., "Seasons Greetings", OSI Small Systems Journal 1, No. 5, p 12 (Nov. 1977)

Program using PEEK and POKE instruction to present a video message.
220. Anon, "ASCII Files under OS-65D", OSI Small Systems Journal 1, No. 5, pp 13-15 (Nov. 1977)

Auxilliary assistance program for a file system.
221. Anon, "BASIC in ROMS", New Product Announcement, OSI Small Systems Journal, 1, No. 5, p 15 (Nov. 1977)

The BASIC in ROM set No 65 AB including 4 ROMS, one EPROM for the 6502 system; Another version 65VB for 440 Video system also available. Either version is $\$ 99$.
222. Struve, Bill, "A $\$ 19$ Music Interface", Byte 2, No. 12, pp 48-69, 170-171 (Dec. 1977)

Some theory and a KIM-1 interface for computer/music addicts.
223. Gordon, H.T., "The XF and X7 Instructions of the MOS Technology 6502", Byte Magazine 2, No. 12, p 72 (Dec. 1977)

A look at some of the unlisted instructions available in the 6502.
224. Forethought Products, PO Box 386, Coburg, OR 97401, Kilobaud, No. 12, p 15 (Dec. 1977), New Product Announcement.

A new board that makes S-100 (Altair/Imsai) type boards compatible with KIM. Motherboard has 8 slots, and does not alter the operation of KIM in any way.
225. Lancaster, Don, "TVT Hardware Design", Kilobaud, No. 12, pp 30-34 (Dec 1977) Part 1; instruction decoder and scan. Taken from Lancaster's new book, "The Cheap Video Cookbook" on the TVT-6L.
226. Blankenship, John, "Expand Your KIM!", Kilobaud, No 12, pp 36-42 (Dec 1977) Part 2 discusses cabinet, nuts and bolts, in this series.
227. Byrd, David A., "TVT-6 Display Uncrowding", Popular Electronics 12, No. 6, p 6 (Dec. 1977)

Gives a technique for correction of a crowding of the display in Lancaster's TVT-6 Video Display.
228. Pittelkau, Clifton W., "The Bionic Clock!", 73 Magazine, No. 208, pp 102-105 (Jan. 1978) \}

Software to add a real time clock to your KIM.
229. Eaton, John, "Growing with KIM", Kilobaud, No. 13, pp 36-39 (Jan. 1978)

Expansion PC Board provides compatibility with S-100 bus.
230. Chamberlin, Hal, "Software Keyboard Interface", Kilobaud, No. 13, pp 98105 (Jan. 1978)

Shows how with a minimum of hardware and minimum cost.
231. Kraul, Douglas R., "Designing Multichannel Analog Interfaces", Byte 2, No. 2, pp 18-23 (June, 1977)

Hardware and software for an 8-channel analog I/O.
232. Fylstra, Dan, "Interfacing the IBM Selectric Keyboard Printer-Teaching KIM to Type", Byte 2, No. 6, pp 46-52, 133-139 (June 1977)

Hardware and software for hooking KIM up to a Selectric.
233. Jobs, Steven, "Interfacing the Apple Computer", Interface Age 1, No. 11, pp 65-66 (Oct. 1976) Interfacing with a printer.
234. Wozniak, Steve and Baum, Allen, "A 6502 Disassembler from Apple", DDJ 1, No. 8, pp 22-25 (Sept. 1976)

Displays single or sequential 6502 instructions in mnemonic form.
235. Grater, Robert, "A Teletype Alternative", Kilobaud, No. 1, pp 114-116 (Jan77) Convert parallel input TVT to serial operation, for KIM.
236. Anon. "Errata to Zieglers 6502 Bug Program", DDJ 1, No. 8, p 33 (Sept. 1976) Corrections for the listing given earlier in DDJ 1, No. 3.
237. Parks, Don, "Adding PlOP to your System", Kilobaud, No. 5, p 98 (May 1977) A 6502 noisemaker for computer games.
238. Rankin, Roy, "Errata for Rankin's 6502 Floating Point Routines", DDJ 1, No. 10, p 57 (Nov/Dec, 1976)

Correction of a bug found in his earlier routine published in DDJ 1, No.7.
239. Lancaster, Don, "Build the TVT-6, Part II", Popular Electronics 12, No. 2 pp 49-55 (August, 1977)

System debugging, software, and how to interface to KIM and other systems.
240. The Data Mart, 914 East Waverly Drive, Arlington Heights, IL 60004, New Product Announcement, "Real Time Clock", On Line 2, No. 18, p 11 (Jan 18,1978) Real Time Clock and Calendar for 6502. Assembled $\$ 95$.
241. Optimal Technology, Inc., Blue Wood 127, Earlysville, VA 22936, Hardware Announcement: PROM Programmer, On Line 2, No. 18, p 11 (Jan 18, 1978) Programmer for KIM programs both the 2708 and 2716 EPROMS. Runs on all 650X systems.
242. Trageser, Jim, "TVT-6L Correction", Kilobaud, No. 12, p 123 (Dec. 1977) Corrections for the June 1977 article by Lancaster.
243. Meyers, Michael J., "Dedicated Controllers - There is Money to be Made", Kilobaud, No. 10, pp 84-9? (Oct. 1977)

Hobbyists should take advantages of opportunities to make money with their KIM or other micro.
244. Burhams, R.W., "Consider a MITE Printer", Kilobaud, No. 11, pp 38-42, (Nov. 1977)

At $\$ 276$, the Mite Expandor is an alternative to the ASR-33 TTY.
245. Penhollow, Bert G.H., "Binary to BCD Conversion for Microprocessors", Electronic Design, p 212 (Oct. 11, 1977) Packs the units and tens into one byte.
246. Chamberlain, Hal, "Computer Bits: Computer Music Part II", Popular Electronics 10 , No. 4, pp 88-91 (Oct. 1977)

A description of music techniques which have been implemented on the KIM-1 DAC board. Also discusses generation of Touch Tone codes.
247. Chamberlain, Hal, "Computer Bits: Computer Music Part In, Popular Electronics 10, No. 3, pp 116-119 (Sept. 1977) Timed loop techniques for computer music programs.
248. Anon., "74 Megabyte Disc Review", OSI Small Systems Journal 1, No. 6, pp pp 2-6 (Dec. 1977)

OSI offers the 74 megabyte CD-74 disc drive for small computers. Has four aluminum disc platters about $12^{\prime \prime}$ diameter. $\$ 6000$. 6502 Related.
249. Anon., "Article Sponsorship Program", OSI Small Systems Journal 1, No. 6, p 7 (Dec. 1977)

OSI will pay for and provide technical assistance for articles on OSI equipment or programs to be published in computer magazines. 6502 Related.
250. Anon., "1K Corner", OSI Small Systems Journal 1, No. 6, p 7 (Dec 1977) Short Program for PRIME NUMBER GENERATOR.
251. Owens, Gerald, "Shoot the Gluck", OSI Small Systems Journal 1, No. 6 pp 8-10 (Dec. 1977)

A game for the 12 K Challenger with video.
252. Anon., "Floppy Disk Users Group", OSI Small Systems Journal 1, No. 6 p 10 (Dec. 1977)

OSI has formed a users group to redistribute user-contributed software on diskettes. The first group of 6502 machine code programs ( 12 listings) is now avallable.
253. Anon., "Terminal/Cassette DOS Input Routine", OSI Small Systems Journal 1, No. 6 pp 11-12 (Dec. 1977)

Program for reloading or transferring program source code.
254. Anon., "New Diskette Software packages", OSI Small Systems Journal 1, No. 6, p 12, (Dec. 1977)

Work Processor WP-1 and WP-1A is a complete word processor. OS-65D Version 2.0 with Nine-digit BASIC. Disk-Test provides a quick functional check of the 6502 computer system.
255. Anon., "Bank Accounts", OSI Small Systems Journal 1, No. 6, pp13-15(Dec 1977) Two practical programs: CHECKBOOK ACCOUNT and SAVINGS ACCOUNT.
256. Fylstra, Dan, "SWEETS for KIM", Byte 3, No. 2, pp 62-77 (Feb. 1978) SWEETS, a Simple Way to Enter, Edit and Iest Software, is a small text editor and assembler which operates on hexadecimal code and which is designed to fit in the KIM-1's 1 K byte small memory while leaving room for the user's programs.
257. Feagans, John, "A Slightly Sour SWEET 16", Byte 3, No. 2, p 93 (Feb. 1978) Correction of a slight bug in the Wozniak article in Byte, Nov. 1977.
Leasia, John D., "Random Errors", Byte 3, No. 2, p 93 (Feb. 1978) Correction of an error in the pseudorandom number generator shown earlier in Byte, Nov. 1977, p 218.
259. Kathryn Atwood Enterprises, P.O. Box 5203, Orange, CA 92667, Byte 3, No. 2, p 187 (Feb. 1978), New Product Announcement 4K RAM board, KIM interface and Mother Board.
260. Electronics Warehouse Inc., 1603 Aviation Blvd., Redondo Beach CA 90278, New Product Announcement.

Apple II I/O Board Kit plugs into slot of Apple II Mother Board. Pittelkau, Clifton W., "KIM-1 Can Do It!", 73 Magazine, No. 209, pp 68-71 (Feb. 1978) Adapting a KIM-1 to function as a versatile RTTY terminal at nominal cost. O'Reilly, Francis J., "Looking for a Micro?", 73 Magazine, No. 209, pp 76-77, (Feb. 1978) Pro's and Con's of the KIM-1 as a micro.
263. Bridge, Theodore E., "A KIM-1 Disassembler", DDJ 2, No. 10, Issue 20 , pp 12-13 (Nov.-Dec. 1977) A modification of Wozniak's earlier 6502 disassembler.
264. Eaton, John, "MATHPAC: A Kimath Supplement", DDJ 2, No. 10, Issue 20 , pp 15-21 (Nov.-Dec. 1977) MATHPAC is designed to increase the power of a 6502 system. It takes the power of KIMATH and gives it to the user. The user's I/O ASCII device turns the system into a scientific calculator.
265. Osborne, Adam, "War of the Processors", SCCS Interface 1, No. 6, pp 14-17, (May, 1976)

Traces evolution of major microprocessors, including 6502 and compares their computing power.
266. Anon., "KIM-1, A complete Microcomputer System for $\$ 245$ ", SCCS Interface 1, No. 6, pp 44-45 (May, 1976)

A new products announcement for KIM-1.
267. Teener, Mike, "Bits and Byters", SCCS Interface 1, No. 6, p 58 (May, 1976) Historical note recaps Motorola's suit against MOS Technology over the 6502's predecessor.
268. MOS Technology, Inc., 950 Rittenhouse Road, Norristown, PA 19401, KIM Application Note \#107702, "S-100 to KIM-4 Bus Adapter", Mechanical details of a simple adapter that will plug into the KIM-4 Mother Board and which will accept certain compatible S-100 boards such as the Kent-Moore No. 60083 video display Doard or the Kent-Moore No. 60082 4K static RAM board.
269. MOS Technology, Inc., 950 Rittenhouse Road, Norristown, PA 19401, KIM Application Note \#111477, "Using KIM as a Dedicated Controller" The KIM itself can be used as a very low cost controller with the addition of a PROM, a power-on-reset modification, and some additional circuitry to transfer control to the added PROM upon power-up.
270. MOS Technology, Inc., 950 Rittenhouse Road, Norristown PA 19401, KIM Application Note \#117701, "Digital-Analog and Analog-Digital Conversion Using the KIM-1"

This is essentially the same as Reference \#172 on DeJong's article in MICRO No. 2. Uses a 1408 D/A converter with KIM together with hardware and software for $D-A$ and $A-D$ as well as software to store the A/D converter output and recall converted data, emulating a storage oscilloscope.
271. MOS Technology, Inc., 950 Rittenhouse Road, Norristown, PA 19401, KIM Application Note \#771121, "Software Routines for TVT"

Machine Language program to use with external keyboard.
272. Optimal Technology, Inc., Blue Wood 127, Earlysville, VA 22936, On Line 3, No. 1, p 1 (Feb. 8, 1978). New Product Announcement.

2708/16 EPROM PROGRAMMER for KIM-1. Requires 1-1/2 I/O Ports. Assembled and tested $\$ 59.95$. Kit $\$ 49.95$.
273. Purser, POB 466, El Dorado, CA 95623, On Line 3, No.1, p 3 (Feb. 8, 1978) Free Guidelines for writing programs for the TRS-80 and PET and then selling them to Radio Shack and Commodore. Send SASE.
274. Personal Software, P.O. Box 136-03, Cambridge MA 02138

On-Line 3 No 1 pg 4 (Feb. 8., 1987) New Product Announcement.
Four full length games on cassette for PET or TRS-80.
POKER, ONE QUEEN, KINGDOM, MATADOR; $\$ 9.95$ for all four. STIMULATING SIMULATIONS by Dr. C.W. Engel, and additional entertainment personal finance/investment, and other systems programs including a 6502 Assembler in BASIC.
275. 6502 Program Exchange, 2920 Moana, Reno, NV 89509, Kilobaud, p 7 (Mar. 1978) Announcement of new 6502 Software including an extended version of FOCAL called FCL 65E (6.5K). Also a Mini-Manual to get you started on TIM or KIM systems.
276. Eaton, John, "Corrections", Kilobaud, No. 15, p 12 (March, 1978) Note on the availability of drilled PC boards for Eatons' KIM expansion article in January 1978 Kilobaud.
277. Scogin, Tom, "AppleSOFT Benchmarks: Fast!", Kilobaud, No. 15, p 12 (Mar 78) Gives times for seven benchmark programs using Apple-II Integer and Apple-II AppleSOFT versions of BASIC.
278. Blankenship, John, "Expand Your KIM!", Part 4., Kilobaud, No. 15, pp 84-88 (March, 1978)

Part four of this series uses a $\$ 10$ circuit board with a SWTP keyboard and a PR-40 printer as a miniature teletype.
279. Zaks, Rodney, "Micro History", Personal Computing 2, No. 2, pp 31-35, (Feb., 1978)

History of microprocessors. Has a very small paragraph on the MOS Technology 650X family.
280. DeJong, Marvin L., "Employing the KIM-1 Microcomputer as a Timer and Data Logging Module", MICRO No. 3, pp 3-7 (Feb. - Mar., 1978) System for logging the time of up to 75 events to the nearest 100 microseconds or to other time increments, and later displaying these times on the KIM-1 display.
281. Carpenter, C.R., "Machine Language used in 'Ludwig von Apple II'", MICRO, No. 3, p 8 (Feb. - Mar. 1978)

Notes on an assembled version of the machine language used by Schwartz, MICRO, No. 2, p 19 in his music program.
282. Carpenter, C.R., "Printing with the Apple II", MICRO, No. 3, pp13-16, (Feb-Mar, 1978)

Hard-copy output from the Apple II using a TELPAR thermal printer, a simple one-transistor adapter circuit and a machine language printing routine.
283. Foreman, Evan H., P.O. Drawer F, Mobile, AL 36601, "The PET Shop", MICRO, No. 3, p 10 (Feb. - Mar. 1978)

Foreman offers to trade five game programs for the PET on a one-for-one basis.
284. Floto, Charles, "The PET VET Tackles Data Files", MICRO, No. 3, pp 9-10, (Feb. - Mar. 1978)

Discusses problems some have encountered in recording data files on tape and reading the information back in. Floto, in his capacity as the PET VET, offers his services on problems met with specific applications of PET.
285. Tater, Gary L., "Hold That Data", MICRO, No. 3, p 11 (Feb. - Mar. 1978) Program to stop data on the video terminal by pressing a key. Handy for examining data during a disassembly or a long directory program.
286. Tripp, Robert M., "Typesetting on a 6502 System", MICRO, No. 3, pp 19-24, (Feb.-Mar. 1978)

A program for "justification" of copy to be printed.
287. Tater, Gary L., "TIM Meets the S100 Bus", MICRO, No. 3, pp 25-26 (Feb.-Mar. 1978)

A bare-bones TIM S100 board to use with a terminal such as the CT-64 from SWTP. Holt, Rod, "The Apple II Power Supply Revisited", MICRO, No. 3, p 28 (Feb.-Mar. 1978)

It is pointed out that the Apple II power supply, although small in physical size, is a switching type which runs cool and is sufficient to run an Apple II with several extra cards plugged into the system. Anon, "Microbes-Tiny Bugs in Previous Micros", MICRO, No. 3, p 28 (Feb-Mar) Corrections for Ultratape, MICRO No. 1, p 13; Making Music with the KIM, MICRO No. 2, p 7; and Important Addresses of KIM-1, MICRO No. 2, p 30.
290. Husbands, Charles R., "A Simple Frequency Counter Using the KIM-1", MICRO No. 3, pp 29-32 (Feb.-Mar. 1978)

The use of KIM-1 as a counter operating over the range of 500 Hz to above 15 KHz .
291. Dial, William, "6502 Bibliography-Part II", MICRO, No. 3, pp 33-36 (Feb-Mar) The second segment of this bibliography covers references 129 to 179 of the rapidly growing 6502 literature.
292. DeJong, Marvin L., "Lighting the KIM-1 Display", MICRO, No. 3, Back Cover. Information on how to use the KIM-1 seven-segment display.
293. Anon, "Software Sources: 6502 Executive for KIM-1", Popular Electronics 13 No. 3, p 98 (March 1978)
Adaptable to any 6502 system, this Executive is designed for KIM-1 with 4 K or more and TTY or TVT interface. $\$ 25$ for listing. From Innovative Software, Inc., 3007 Casa Bonita Dr., Bonita, CA 92002.
294. Pollock, James W., "Microprocessors: A Microprocessor controlled CW Keyboard" Ham Radio 11, No. 1, pp 81-87 (Jan. 1978) A preprogrammed microcomputer is designed to function as a Morse Code keyboard. Uses a MOS Technology MCS6504 which is a software compatible cousin to the 6502.
295. Connecticut Microcomputer, 150 Pocono Rd., Brookfield, CT 06804, New Product Announcement, "RS-232 Adapter for KIM", DDJ 3, No. 21, p 3 (Jan '78) The ADApter converts KIM's 20 ma. current loop port to an RS-232 port without affecting the baud rate. $\$ 24.50$
296. Schick, Paul, "Unsupported OPCODE Pitfalls", DDJ 3, No. 21, p 3 (Jan 1978) Comments on the earlier article on 650X Opcodes: DDJ, Aug 1977.
297. Moser, Carl, "Memory Test for 6502", DDJ 3, No. 21, pp 4-5, (Jan 1978) A program which tests RAM memory in a 6502 based system. I/O is arranged for 6502 TIM based system but can be easily changed.
298. Smith, Stephen P., "Challenging Challenger's ROMS", DDJ 3, No. 21, p 6 (Jan) Using the PREK function of the OSI Microsoft BASIC, a disassembler to convert stored bytes in the PROMs or ROMs has been devised.
299. Computers One, PO Box 7148, Honolulu, HI 96821, New Product Announcement, On Line 3, No. 2, p 4 (March 1, 1978) Pre-recorded programs for PET. "HUSTLERS" includes a number of business oriented programs for checking accounts, rent accounts, legal dairy and trust accounts.
300. Lufkin, C.R., 315 Dominion Dr., Newport News, VA 23602, On-Line 3, No 2, p 5 (March 1, 1978)

FITABP is Federal Income Tax Program for PET owners with 8K. Prints out form 1040 Schedule A and B. $4: 42$

## A IIM BEEPER

Gerald C. Jentins 774 Twin Branch Drive Birmingham, AL 352 z6

A short blast or two of audio for load errors, end-of-line, etc., is nice to have. This routine requires a simple audio amplifier such as the one in the KIM-1 User Manual, page 57, or the one shown below. Also needed is a latched output port, again such as those on the KIM-1, and a programmable timer.

Enter the routine with the number of blasts in the $X$ register. Change the tone to suit by changing contents of NOTE, \$0114.


| 0100 | A9 | FF |  | BEEP | LDAIM | TIME | START TIMER FOR $1 / 4$ SECOND TONE |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 0102 | 8D | 07 | 17 |  | STA | TIMER | USING INTERVAL TIMER |
| 0105 | A9 | 01 |  |  | LDAIM | \$01 | SET OUTPUT TONE OFF |
| 0107 | 8D | 02 | 17 |  | STA | PBD |  |
| 010A | 8D | 03 | 17 |  | STA | PBDD |  |
| 010D | 4D | 02 | 17 | TONE | EOR | PBD | TOGGLE OUTPUT |
| 0110 | 8D | 02 | 17 |  | STA | PBD |  |
| 0113 | AO | C8 |  |  | LDYIM | NOTE | SET TO COUNT FOR NOTE LENGTH |
| 0115 | 88 |  |  | TONEX | DEY |  | \$C8 = 500 HZ |
| 0116 | D0 | FD |  |  | BNE | TONEX | CYCLE IN DOWN COUNTER |
| 0118 | 24 | FF |  |  | BIT | TIME | TEST 1/4 SECOND UP |
| 011A | 10 | F1 |  |  | BPL | TONE | CONTINUE TONE IF NOT DONE |
| 011C | A9 | 01 |  |  | LDAIM | \$01 | TURN TONE OFF |
| 011 E | 8D | 02 | 17 |  | STA | PBD |  |
| 0121 | A9 | FF |  |  | LDAIM | TIME | START WAIT BETWEEN BEEPS |
| 0123 | 8D | 07 | 17 |  | STA | TIMER |  |
| 0126 | 2C | 07 | 17 | NOTONE | BIT | TIMER | WAIT FOR TIME OUT |
| 0129 | 10 | FB |  |  | BPL | NOTONE |  |
| 012B | CA |  |  |  | DEX |  | DECREMENT NUMBER OF BEEPS COUNTER |
| 012C | D0 | D2 |  |  | BNE | BEEP | ANOTHER BEEP OR |
| 012E | 60 |  |  |  | RTS |  | DONE. RETURN TO CALLING ROUTINE |

## A Few Notes:

1. Although the above version is assembled at $\$ 0100$, it is relocatable and can be placed anywhere in memory.
2. The calling sequence for BEEPER is:
put number of beeps into the $X$ register JSR BEEPER
on return $A=\$ F F, X=\$ 00$, and $Y=\$ 00$

## KEYBOARD WIZARDRY



ENGINEERED SPECIFICALLY FOR THE CHERRY-PRO KEYBOARD

- Space Provided for Power Supply and Additional Boards
- Easy Access to Connectors
- Keyboard Positioned for Ease of Operation
EASILY ASSEMBLED
- Requires Absolutely No Alteration of the PRO Keyboard
- All Fasteners Provided
- Goes Together in Minutes with a Small Screwdriver

ATTRACTIVE FUNCTIONAL PACKAGE

- Professional Appearance
- Four Color Combinations
- Improves Man/Machine Interface MADE OF HIGH IMPACT STRENGTH THERMOFORMED PLASTIC
- Kydex 100*
- Durable
- Molded-In Color
- Non-Conductive

AVAILABLE FROM STOCK

- Allow Two to Three Weeks for Processing and Delivery
- No COD's Please
- Dealer Inquiries Invited

TO ORDER: 1. Fill in this Coupon (Print or Type Please)
2. Attach Check or Money Order and Mail to:

NAME $\qquad$
STREET $\qquad$
CITY $\qquad$
STATE $\qquad$ ZIP $\qquad$
Please Ship Prepaid $\qquad$ SKB 1-1(s)

> @ \$33.75 Each

California Residents please pay $\$ 35.94$ (Includes Sales Tax)

[^0]
# AI APPLE-II PROGRANMER'S GUIDE <br> [You Can Get There From Here!] 

Rick Auricchio
59 Plymouth Avenue Maplewood, NJ 07040

Most of the power of the APPLE-II comes in a "secret" form - almost undocumented software. After several months of coding, experimenting, digging, and writing to APPLE, most of the APPLE's pertinent software details have come to light.

Although most of the ROM software has been printed in the APPLE Reference Manual, its Integer Basic has not been listed; as a result, this article will be limited to Monitor software. Perhaps when a source listing of Integer Basic becomes available, we'll be able to interface with some of its many routines.

## First Things First

When I took delivery of my Apple (July 1977), all I had was a "preliminary" manual - no goodies like listings or programming examples. My first letter to Apple brought a listing of the Monitor. Seeing what appeared to be a big jumble of instructions, I set out dividing the listing into logical routines while deciphering their input and output parameters. Once this was done, I could look at portions of the code without becoming dizzy.

The Monitor's code suffers from a few ills:

1 Subroutines lack a descriptive "preamble" stating function, calling seqquences, and interface details.

2 Many subroutines have several entry points, each of which does something slightly different.

3 Useful routines are not documented in a concise form for user access.

I will concede that, while using a "shoehorn" to squeeze as much function as possible into those tiny ROM's, some shortcuts are to be expected. However, those valuable Comment Cards don't use up any memory space in the finished product - 'nuff said.

## The Good Stuff

The best way to present the Apple's software interface details is to describe them in tabular form, with further explanation about the more complex ones. The following tables will be found on the pack cover of this issue:

Table 1 outlines the important data areas used by the Monitor. These fields are used both internally by the Monitor, and in user communication with many Monitor routines. Not all of the data fields are listed in Table 1.

Table 2 gives a quick description of most of the useful Monitor routines: it contains Name, Location, Function, Input/Output parameters, and Volatile (clobbered) Registers.

Don't hesitate to experiment with these routines - since all the important software is in ROM, you can't clobber anything by trying them out (except what you might have in RAM, so beware).

## Using the "User Exits"

The Monitor provides a few nice User Exits for us to get our hands into the Monitor. With these, it is a simple matter to "hook in" special I/O and command-processing routines to extend the Apple's capabilities.

Two of the most useful exits are the KEYIN and COUT exits. These routines, central to the function of the Monitor, are called to read the keyboard and output characters to the screen. By placing the address of a user routine in CSWH/L or KSWH/L, we will get control from the Monitor whenever it attempts to read the keys or output to the screen.

As an example of this exit!s action, try this: with no I/O board in I/O Slot 5, key-in "Kc5" (control K, followed by 5, then Return). You'll have to hit Reset to stop the system.

Here's what happened: setting the keyboard to device 5 causes the Monitor to install \$C500 as the "user-exit" address in KSWH/L. This, of course, is the address assigned to I/O Slot 5. Since no board is present, a BRK opcode eventually occurs; the Monitor prints the break and the registers, then reads for another command. Since we still exit to $\$ C 500$, the process repeats itself endlessly. Reset removes both user exits; you must "re-hook" them after every Reset.

These two exits can enable user editing of keyboard input, printer driver programs, and many other ideas. Their use is limited to your ingenuity.

Another useful exit is the Control $Y$ command exit. Upon recognition of Control Y, the Monitor issues a JSR to location $\$ 03 \mathrm{~F} 8$. Here the user can process commands by scanning the original typed line or reading another. This exit is often very useful as a shorthand method of running a program. For example, when you're going back and forth between the Monitor and the MiniAssembler, typing "F666G" is a bit tiresome. By placing a JMP \$F666 in location $\$ 03 F 8$, you can enter the Mini-Assembler via a simple Control Y.

Upon being entered from the Monitor at \$03F8, the registers are garbage. Locations A1 and A2 contain converted values from the command (if any), and an RTS gets you neatly back into the Monitor. Figure 1 shows this in more detail.

Figure 1: Control Y Interface

Command typed:
-1234.F5A7YC
Upon entry at $\$ 03 \mathrm{~F} 8$, the following exists:

A1L (\$3C) contains \$34
A1H (\$3D) contains \$12
A2L (\$3E) contains \$A7
A2H ( $\$ 3 \mathrm{~F}$ ) contains $\$ \mathrm{~F} 5$

## Hardware Features

One of the best hardware facilities of the Apple-II, the screen display, is also the "darkest" - somewhat unknown. Here's what I've found out about it.

The screen buffer resides in memory pages 4 through 7, locations $\$ 0400$ through about \$07F8. The Secondary screen page, although not accessed by the Monitor, occupies locations $\$ 0800$ through \$0BF8. Screen lines are not in sequential memory order; rather, they are addressed by a somewhat complex calculation carried out in the routine BASCALC. What BASCALC does is to compute the base address for a particular line and save it; whenever the cursor's vertical position changes, BASCALC recomputes the base address. Characters are stored into the screen buffer by adding the base address to the cursor's horizontal position.

I haven't made too much use of directly storing characters into the screen buffer; usually just storing new cursor coordinates will do the trick via the Monitor routines. Be careful, though only change vertical position via the VTAB routine since the base address must get recomputed!

Characters themselves are internally stored in 6-bit format in the screen buffer. Bit 7 ( $\$ 80$ ), when set, forces normal (white-on-black) video display for the character. If Bit 7 is reset, the character appears inverse (black-on-white) video. Bit 6 ( $\$ 40$ ), when set, enables blinking for the character; this occurs only if Bit 7 is off. Thus an ASCII "A" in normal mode is $\$ 81$; in inverse mode, $\$ 01$; in blinking mode, $\$ 41$.

Reading the keyboard via location $\$ 0000$ is easy; if Bit $7(\$ 80)$ is set, a key has been pressed. Bits $0-6$ are the ASCII keycode. In order to enable the keyboard again, its strobe must be cleared by accessing location $\$ \mathrm{CO} 10$. Since the keyboard is directly accessible, there is no reason you can't do "special" things in a user program based on some keyboard input - if you get keys directly from the keyboard, you can bypass ALL of the Control and Escape functions.

# AI APPLE II PROGRAMIER'S GUIDE 

Rick Auricohio
59 Plymouth Avenue Maplewood, NJ 07040

MONITOR Data Areas in Page Zero

Name
Loc.
Function

WNDLEFT 20
WNDWDTH 21
WNDTOP 22
WNDBTM 23
$\mathrm{CH} \quad 24$
CV 25
COLOR 30
INVFLG 32
PROMPT 33
CSWL 36
CSWH 37
KSWL 38
KSWH 39
PCL 3A
$\mathrm{PCH} \quad 3 \mathrm{~B}$
A1L 3C
A1H 3D
A2L 3E
$\mathrm{A} 2 \mathrm{H} \quad 3 \mathrm{~F}$
A3L 40
A3H 41
A4L 42
$\mathrm{A} 4 \mathrm{H} \quad 43$
A5L 44
$\mathrm{A} 5 \mathrm{H} \quad 45$
$A C C \quad 45$
XREG 46
YREG 47
STATUS 48
SPNT 49

Serolling window: left side ( $0-\$ 27$ )
Sorolling window: width (1-\$28)
Scrolling window: top line ( $0-\$ 16$ )
Scrolling window: bottom line (1-\$17)
Cursor: horizontal position ( $0-\$ 27$ )
Cursor: vertical position ( $0-\$ 17$ )
Current COLOR for PLOT/HLIN/VLIN functions Video Format Control Mask: \$FF=Normal, \$7F:Blinking, \$3F=Inverse
Prompt character: printed on GETLN CALL Low PC for user exit on COUT routine High PC for user exit on COUT routine Low PC for user exit on KEYIN routine High PC for user exit on KEYIN routine Low User PC saved here on BRK to Monitor High User PC saved here on BRK to Monitor A1 to A5 are pairs of Monitor work bytes

User AC saved here on BRK to Monitor User X saved here on BRK to Monitor User $Y$ saved here on BRK to Monitor User $P$ status saved here on BRK to Monitor User Stack Pointer saved here on BRK

Page $2(\$ 0200-\$ 02 F F)$ is used as the KEYIN Buffer.
Pages 4-7 ( $\$ 0400-\$ 07 \mathrm{FF})$ are used as the Screen Buffer.
Page 8 ( $\$ 0800-\$ 08 \mathrm{FF}$ ) is the "secondary" Soreen Buffer.

Table 1.

## MONITOR ROUTINES

| Name | Loc． | Steps On | Function |
| :---: | :---: | :---: | :---: |
| PLOT | F800 | AC | Plot a point．COLOR contains color in both halves of byte（ $\$ 00-\$ F F$ ）．AC：y－coord，Y：x－coord． |
| CLRSCR | F832 | $A C, Y$ | Clear screen－graphics mode． |
| SCRN | F871 | AC | Get screen color．AC：y－coord，Y：x－coord． |
| INSTDSP | F8D0 | ALL | Disassemble instruction at PCH／PCL． |
| PRNTYX | F940 | AC | Print contents of $Y$ and $X$ as 4 hex digits． |
| PRBL2 | F94C | AC，X | Print blanks：$X$ is number to print． |
| PREAD | FB1E | $A C, Y$ | Read paddle．$X$ ：paddle number $0-3$ ． |
| SETTXT | FB39 | AC | Set TEXT mode． |
| SETGR | FB40 | AC | Set GRAPHIC mode（GA）． |
| VTAB | FC22 | AC | VTAB to row in AC（ $0-\$ 17$ ）． |
| CLREOP | FC42 | AC，Y | Clear to end－of－page． |
| HOME | FC58 | AC，Y | Home cursor and clear screen． |
| SCROLL | FC70 | $A C, Y$ | Scroll up one line． |
| CLREOL | FC9C | $A_{\text {AC，}} \mathrm{Y}$ | Clear to end－of－line． |
| NXTA4 | FCB4 | AC | Increment A4（16 bits），then do NXTA1． |
| NXTA1 | FCBA | AC | Increment A1（16 bits）．Set carry if result $>=\mathrm{A} 2$ ， |
| RDKEY | FDOC | AC， Y | Get a key from the keyboard． |
| RDCHAR | FD35 | AC，Y | Get a key，also handles ESCAPE functions． |
| GETLN | FD6A | ALL | Get a line of text from the keyboard，up to the carriage return．Normal mode for Monitor．X returned with number of characters typed in． |
| CROUT | FD8E | AC，Y | Print a carriage return． |
| PRBYTE | FDDA | AC | Print contents of AC as 2 hex digits． |
| COUT | FDED | AC， $\mathbf{Y}$ | Print character in AC；also works for CR，BS，etc． |
| PRERR | FF2D | AC，Y | Print＂ERR＂and bell． |
| BELL | FF3A | AC，$Y$ | Print bell． |
| RESET | FF59 | －－ | RESET entry to Monitor－initialize． |
| MON | FF65 | －－ | Normal entry to＇top＇of Monitor when running． |
| SWEET 16 | F689 | None | SWEET16 is a 16 －bit machine language interpreter． ［See：SWEET16：The 6502 Dream Machine，Steve Wozniak，］ ［BYTE，Vol．2，No．11，November 1977，pages 150－159．］ |

Table 2.


[^0]:    * TM Rohm \& Haas

